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FDMA in point-to-multipoint fibre access systems for non-residential applications

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ABSTRACT

Optical access networks are seeing growing applications for use cases beyond residential, for example in campus and as Industry 4.0 intra-factory networks, which introduce different requirements in terms of bandwidth delivery and latency. We present an uplink access system with simultaneous transmission and detection of several users by means of frequency division multiplexing (FDM). We demonstrate a multiple uplink access system with differential binary phase shift keying (DBPSK) signals and coherent detection that targets a low and deterministic latency. We achieve receiver (Rx) sensitivities of -43.5dBm, -40dBm, and -34dBm at a pre forward error correction (FEC) bit error ratio (BER) of 10^{-3} at 2.5 GBaud, 5 GBaud, and 8 GBaud respectively after 20km of fibre with coherent detection. Furthermore, we show the possibility of employing time-division multiplexing (TDM) within the frequency bands. We also present real-time services showing that the system can allow latency-sensitive and best-effort applications to share the network.

Keywords: optical access, point to multipoint, industry 4.0, FDM, DPSK, TDM

1. INTRODUCTION

The industry 4.0 calls for combined machine to machine (M2M) communications, computer vision, and edge computing to realize automatic and intelligent manufacturing, which could potentially benefit from optical interconnects [1]. Optical networks are preferable to conventional copper or wireless communications in industrial environments where electromagnetic interference can be a significant source of interference and bitrates as high as 10 Gb/s per machine or user are required [2].

Point to multi-point (P2MP) network architectures are attractive due to their reduced cost of operation. In particular, passive optical networks (PONs) have been successful in delivering high bandwidth (BW) to residential users using a P2MP architecture [3], [4]. However, PON systems based on time division multiplexing (TDM) would encounter significant challenges regarding latency and jitter in time-sensitive industrial applications due to its burst-mode nature in the uplink transmission. The service requirements for industrial M2M, such as low and deterministic latency, require a different P2MP system design. It is also anticipated that computer vision applications will entail a large uplink capacity, needing symmetric data rates or possibly even higher uplink than downlink data rates.

Frequency division multiplexing (FDM) is a possible solution for these systems, where several users or devices could transmit data simultaneously in continuous mode with dedicated BW and deterministic low latency [5], [6]. In addition, a single integrated coherent receiver (ICR) at a centralized unit can efficiently detect multiple sub-channels when they are carefully aligned in frequency domain as shown in [5]-[7]. The uplink coherent detection can offer significant flexibility to each device to support different BW demands by choosing the modulation format and symbol rate [8]. Furthermore, for the FDM to be an attractive solution, the transmitters (Tx) should be realized via low-cost components.

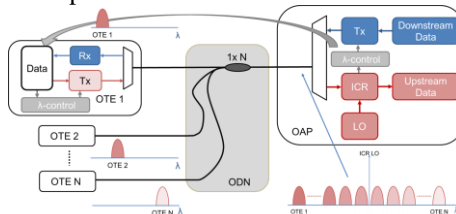


Fig. 1: P2MP access network system utilizing low latency FDMA sub-channels in the upstream.

In this paper, we propose the architecture shown in Fig. 1 in which several optical terminal equipment (OTE) can transmit simultaneously at different, but closely spaced, wavelengths to reduce the queuing time and buffering, effectively creating a FDM access (FDMA) system. The wavelength for each terminal is tuned by temperature, and these are supervised and controlled by the optical aggregation point (OAP). We mainly focus

on the uplink in this paper since we envision that the traffic will flow mainly in this direction. However, a full downlink and uplink FDMA system has been reported in [7] for a future residential PON. The Rx in the OAP is based on a commercial ICR which allows to have high Rx sensitivity and separate the OTE spectra electrically by means of digital signal processing (DSP) [3]-[5]. In the OTE, we employ directly modulated laser (DML) as a low-cost solution Tx, potentially for sensors or cameras. We show the detection of 3 OTEs simultaneously at bitrates of 2.5 Gb/s, 5 Gb/s and 8 Gb/s with Rx sensitivities of -43.5 dBm, -40 dBm, and -34 dBm, respectively, at a pre-FEC BER threshold of 10^{-3} corresponding to RS(255,223) coding with 14.35% overhead and 6.56 dB net coding gain, which is adopted in 10G-EPON. The effect of additional channels, power imbalance and wavelength stability are also presented.

2. EXPERIMENTAL SETUP

Fig. 2 depicts the experimental setup. We firstly generate offline three set of random bit sequences at 2.5 Gb/s, 5 Gb/s, and 8 Gb/s, encode them differentially, and shape them by a root-square raised cosine (RRC) filter with roll-off factor of 0.05. We then upsample the signal and apply a differentiator with a half-bit delay function. The latter is used to obtain a phase-modulated signal from the DML [9], [10]. The real-valued waveforms are loaded into a digital to analogue converter (DAC) with sampling rate of 80 GSa/s. The three electrical signals are amplified and directly modulate three distributed feedback laser (DFB) whose emission wavelengths are tuned and controlled in temperature. By means of a centralized control system, we set the wavelength of each DFB to a different value in order to avoid interference. We then combine the three optical signals through optical couplers and control their power independently through variable optical attenuators (VOA). The whole signal propagates through 20km of single mode fibre (SMF) and another VOA controls the total input power into the ICR. As local oscillator (LO) we use an external cavity laser (ECL), which is effectively the reference for the wavelength control system of the OTE Tx. The four outputs of the ICR with the in-phase and quadrature information in both polarizations are then quantized by the analogue to digital converters (ADC) embedded in a real-time oscilloscope and further processed offline. The Rx DSP comprises a tributary de-skewing followed by an electrical frequency down-conversion of the three detected signals. Once the three signals are in baseband they are low-pass filtered and down-sampled. The resulting real data stream goes through an automatic gain control (AGC), a 4×2 multiple input and multiple output (MIMO) equalizer [11] and carrier phase recovery (CPR) stage. Finally, the BER is calculated by direct error counting.

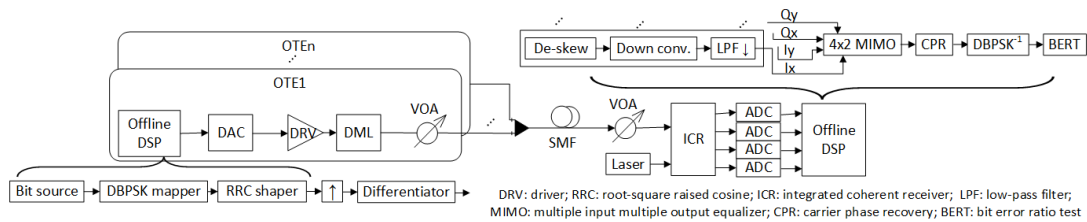


Fig. 2: Experimental setup of upstream coherent FDM P2MP network using DML and transceiver offline DSP.

The ICR is also used to determine the wavelength of each OTE with reference to the LO wavelength. Each DML has a TEC controlled by a consumer micro-controller, which is used to control and tune its wavelength according to the information on the relative wavelength error provided by the OAP Rx. During registration, the OAP first indicates the OTEs to move to the highest possible wavelength by heating. Then, the OTEs slowly cool down and are detected in a registration channel. The OAP then indicates each OTE their current wavelength and where to move following the 0.1nm/°C rule of thumb for DFBs. Once the DMLs are at the correct separation, they transmit information and the OAP continuously monitors and controls their wavelength in a closed loop.

3. RESULTS

The spectrum of the three detected OTE signals after the ADC is shown in Fig. 3. The three signals have the same optical power and Fig. 3(a) and 3(b) display the signals with either a spectral gap or no gap between OTEs, respectively. The OTE data rates are 8 Gb/s, 2.5 Gb/s, and 5 Gb/s (as indicated in Fig. 3 from left to right on the detected signal spectrum). We test different data rates for each OTE to emulate the varying BW demand of OTEs in a real network. We add an extra dummy channel with 12.5 dB higher power than the others, as shown in Fig. 3(c) to emulate a full capacity network as well as possible power difference in P2MP networks due to different optical splitting or launch powers.

The BER vs. received optical power (ROP) performance of DBPSK signals at several experimental configurations is shown in Fig. 4. We firstly measure each channel independently in optical back-to-back (oBtB). At a BER of 10^{-3} , we measure an Rx sensitivity of -45.5 dBm for the 2.5 Gb/s OTE and the 5 Gb/s and 8 Gb/s have penalties of 4.7 dB and 9 dB respectively. While these penalties exceed the theoretical 3 dB and 5 dB for increasing the bitrate, these are explained by the limited BW of the electrical components and the DMLs.

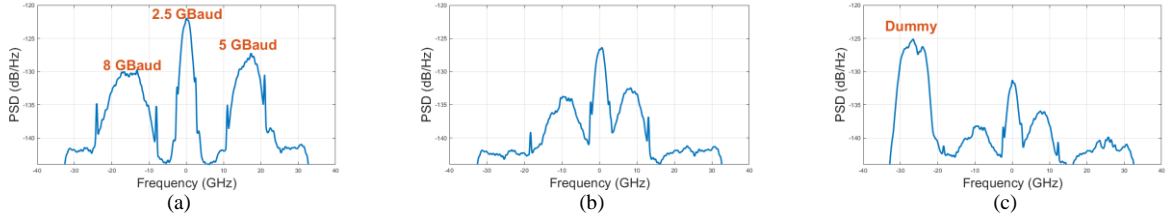


Fig. 3: Spectra of the detected signal with equalized power with (a) and without (b) spectral gap; (c) an additional channel emulating full capacity is added.

We then join the three signals at same optical power and with the centre frequencies separated by 16 GHz, which leaves a spectral gap between adjacent channels as can be seen in Fig. 3(a). After detecting the three signals simultaneously in the ICR, we observe no penalty for the 2.5 Gb/s and 5 Gb/s relative to the independent channel measurement. On the other hand, the 8 Gb/s has 1 dB penalty mainly caused by slight interference from the neighbour channel. Since we apply a strong spectral shaping, we can suppress the spectral gap between the channels (Fig. 3(b)). In this case, there is a penalty of 1 dB for both the 2.5 Gb/s and 5 Gb/s OTEs, and of 1.8 dB for the 8 Gb/s OTE. This penalty is because even with strong shaping in Tx, there is some residual interference between the channels which further indicates the importance of the wavelength control to stabilize the OTE frequency to avoid strong drifts. In our demonstration employing the accurate closed loop thermal tuning and stabilization wavelength drift of less than ± 50 MHz has been measured over an extended time [12].

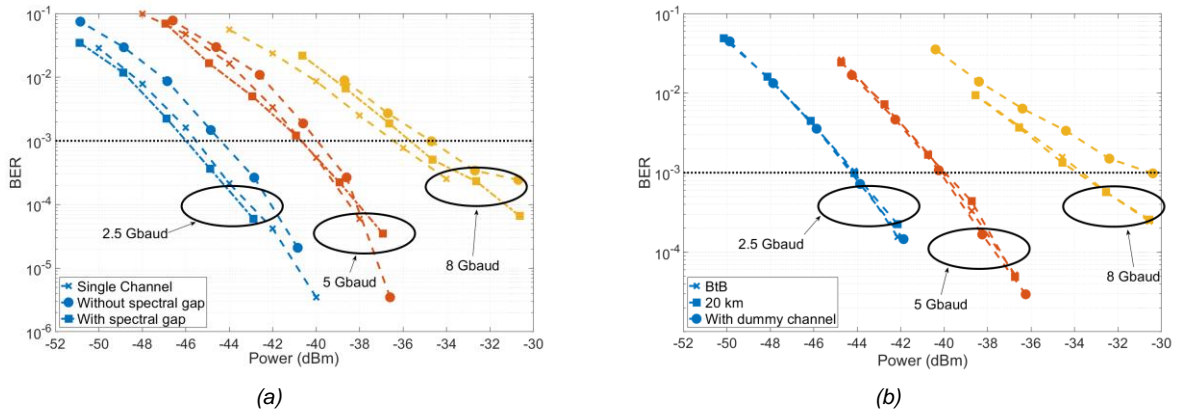


Fig. 4: BER vs. ROP performance for each OTE: (a) impact from channel spectral gap in oBtB; and (b) transmission over 20 km SMF.

In Fig. 4(b) we plot the performance after 20 km SMF transmission. There is no penalty with respect to oBtB, since the symbol rates of the DBPSK signals are relatively low. We obtain power sensitivities of -43.5 dBm, -40 dBm, and -34 dBm for 2.5 Gb/s, 5 Gb/s and 8 Gb/s DBPSK signals, respectively. Fig. 4(b) also shows the impact of having the network at full capacity emulated by adding a dummy channel (Fig. 3(c)) without spectral gap. We observe no penalty for both 2.5 Gb/s and 5 Gb/s signals compared with the case of having only the three OTE signals since the system is limited by shot noise rather than ADC quantization noise. However, the 8 Gb/s OTE shows about 4 dB penalty as part of the emulated dummy channel interferes in the spectrum as seen in Fig. 3(c).

Despite having demonstrated only 3 DML-based OTEs for an overall data rate of 15.5 Gb/s, 3 additional OTEs could be easily allocated in the 60 GHz optical BW system. Alternatively, if all OTEs operate at 2.5 Gb/s, 12 OTEs could be supported for an overall capacity of 30 Gb/s. Further scaling the system up to 60 or 120 Gb/s could be achieved by employing either wider BW ICR or by using higher modulation formats like differential quadrature PSK (DQPSK) or adding polarization multiplexing.

As mentioned above, the wavelength control is of high importance in the proposed architecture. Fig. 5(a) shows the spectra of two OTEs whose emission frequency is monitored and controlled at the OAP. Each of the OTE signals is measured after applying a fast-Fourier transform (FFT) to the samples obtained through the ICR using the LO as reference. We insert a pilot tone in the transmitted signals for identifying the OTEs and aid the wavelength estimation. The pilot tones are located by searching a peak in the spectrum. Then, they are monitored and controlled within ± 50 MHz by sending commands to a Raspberry Pi in the OTE to control its temperature. The minimum frequency window depends on the number of points of the FFT and the repetition rate that is needed.

We further explore the possibility of sharing a frequency channel by using TDM as illustrated in Fig. 5(b). For this experiment, we use three optical modulators, each with a different DFB as light source. Two OTE Tx share a channel and transmit in burst mode while the third one is on an adjacent channel sending data in continuous mode. The bitrate for each OTE is 8 Gb/s with DBPSK format. For the TDM, we add 20 km of SMF in one of

the OTE to delay the burst, so that it can be detected in its corresponding time slot. By using the SMF, we also emulate the transmission effect and different OTE powers arriving into the ICR. Fig. 5(a) depicts the bursts and Fig. 5(c), the Rx graphical unit interface (GUI) of the ICR showing the proper detection of the three OTEs.



Fig. 5 (a) Wavelength control interface and burst received at the OAP; (b) TDMA and FDMA experimental block diagram; (c) Rx GUI with two TDM users detected

As a final remark, in [12] we demonstrated live Ethernet services in a similar setup as the one studied here but with real-time processing. The total latency of the system measured through clock cycles (~ 4 ns) of a field-programmable gate array (FPGA) was about $2.5 \mu\text{s}$, showing that the proposed system can help on keeping a determinist latency.

4. CONCLUSIONS

We experimentally demonstrated a FDM P2MP upstream transmission system using low-cost 2 GHz DMLs at OTEs with flexible bit rates. The impact of channel gap and power distribution were also examined. By considering a pre-FEC threshold BER of 10^{-3} , we achieved Rx sensitivities of -43.5 dBm, -40 dBm and -34 dBm at 2.5 Gb/s, 5 Gb/s, and 8 Gb/s respectively with DBPSK modulation. No transmission penalty was measured after 20km of fibre. The results show the feasibility of such architecture is allowing several users to transmit simultaneously and a deterministic latency of $\sim 2.5 \mu\text{s}$ has been measured. Furthermore the capacity of the individual FDM channels can be shared using TDM which allows a more flexible and efficient allocation of the system capacity for the OTEs that do not have strict latency requirements.

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