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ABSTRACT

Tandem solar cells made of organometal halide perovskite and crystalline silicon cells are one of the most promising routes towards high efficiency low cost photovoltaics. Among the possible architectures, monolithic three-terminal tandems hold the promise of the highest energy/cost figure of merit, by combining the advantage of two- and four-terminal approaches. Recently, three-terminal perovskite/silicon tandems have been reported, based on interdigitated back contact heterojunction silicon cells. Alternative solutions that can be integrated with double-sided contact silicon cells are worth to be investigated in view of their higher compatibility with industrial mass production. In this work, we present a simulation-based proof-of-concept of PVK/Si three-terminal tandem cells that use a heterostructure bipolar transistor structure. The extra terminal is implemented at the common selective layer between the perovskite and silicon subcells, avoiding the use of any recombination layer or tunneling junction. We demonstrate promising device performance through physics-based simulations preliminarily validated against experimental data of other perovskite/silicon tandem technologies reported in literature.

Keywords: perovskite; silicon tandem; multijunction architecture; heterostructure bipolar transistor; textured solar cell; HIT solar cell; three-terminal; double junction.

1. INTRODUCTION

Organometal halide perovskites are among the most promising candidates as top sub-cell for silicon based tandems because of their excellent electrical and optical properties and low-cost technology. In monolithic two-terminal (2T) tandems, the perovskite (PVK) solar cell stack is directly deposited on top of the silicon bottom cell, either with flat or textured surface, and the two sub-cells are electrically connected through a tunnel junction or recombination layer. For maximum power conversion efficiency, the series connection demands that the two subcells generate high and similar currents, making the solar cell performance sensitive to spectral variations and limiting the attainable real world energy yield.

To overcome the current matching constraint while retaining a monolithic structure, a viable alternative is offered by the three terminal (3T) architecture, which features a third contact allowing for independent power extraction from the two subcells. PVK/Si 3T tandem have been reported where the third terminal is implemented at the recombination layer connecting the two subcells, or at the rear side of the silicon cell adopting an interdigitated back contact design. Besides these approaches, the novel multijunction architecture based on the heterojunction bipolar transistor (3T-HBT) structure appears as a promising, yet rather unexplored, route towards 3T monolithic PVK/Si tandems compatible with both heterojunction and homojunction c-Si cells. In the 3T-HBT solar cell, one common layer (base) is shared between the top and bottom subcells’ stack and used to realize an electrical contact for one type of carriers (e.g. holes), while the other type of carrier (e.g. electrons) is collected at the other two terminals (emitter and collector), one for each subcell. This allows for the independent operation of the two sub-cells without the use of any recombination layer, minimizing parasitic optical losses and carrier loss in case of electrical shunts.

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In this work, we present a preliminary design of a PVK/Si 3T-HBT tandem using a c-Si heterojunction bottom cell and investigate its performance with physics-based simulations encompassing optical and transport aspects. The 3T-HBT tandem is developed starting from the fully textured monolithic perovskite/silicon 2T tandem solar cell reported by Sahli et al.\textsuperscript{9} Simulations are firstly verified against the experimental data in\textsuperscript{9} and then used to analyze the performance of the proposed PVK/Si 3T-HBT solar cell with respect to that one of the 2T tandem.

**2. MODEL & EXPERIMENTAL VALIDATION**

We take as reference the fully textured PVK/silicon tandem\textsuperscript{9} shown in Fig. 1. It consists of a PVK top cell and a silicon bottom cell with heterojunction with intrinsic thin-layer (HIT) on both sides, interconnected through a $p^{++}/n^{++}$ nc-Si tunnel junction. The conformal deposition of textured perovskite layers leads not only to a high compatibility with industrial silicon cells with front/rear textures, but also to improved light management.\textsuperscript{1,9} From the top, the 100 nm thick MgF$_2$ layer is the anti-reflection coating layer, and the 110 nm thick indium zinc oxide (IZO) layer acts as transparent front electrode.\textsuperscript{9} The PVK subcell consists of: 10 nm SnO$_2$ layer,\textsuperscript{10,11} 15 nm C$_{60}$\textsuperscript{12} electron selective layer, 440 nm of photo-absorbing mixed perovskite layer\textsuperscript{13–15} and 12 nm thick Spiro-TTB hole transport layer.\textsuperscript{10,14} The HIT subcell consists of 260 $\mu$m n-doped c-Si ($N_D = 2 \times 10^{15}$ cm$^{-3}$)\textsuperscript{16} sandwiched by two thin-films of hydrogenated amorphous silicon: from the top (bottom) 5 (11) nm of n- (p-) doped a-Si:H\textsuperscript{16} and 6 nm of intrinsic a-Si:H. The bottom cell is capped on its rear side by a 70 nm thick indium tin oxide (ITO) layer.

The opto-electronic model is developed within Sentaurus Technology Computer-Aided Design (TCAD) software from Synopsys.\textsuperscript{17} For the textured surfaces, we assume a regular inverted pyramidal texture with silicon characteristic base angle of 54.7$^\circ$ and height of 7$\mu$m.\textsuperscript{16} Taking advantage of the assumed periodicity, optical simulations are done on the smallest symmetry element of the structure, i.e., a single pyramid unit. We assume normal incidence and an input optical power of 100 mW/cm$^2$ (AM1.5G spectrum). Optical simulations are carried out using a hybrid optical model, which combines transfer matrix method (TMM) and Monte Carlo RayTracing to assess both interference effects through the subwavelength multilayered media and scattering effects at the textured surfaces. In more detail, the angle at which the ray is incident on the first layer of the textured surface is passed as input to the TMM solver, which simulates the incidence-angle-dependent propagation across the thin film layers; the resulting optical characteristics, i.e. reflectance, transmittance and absorbance for the two

![Schematic view of the simulated fully textured PVK/HIT-silicon tandem solar cell](image-url)

Figure 1. Schematic view of the simulated fully textured PVK/HIT-silicon tandem solar cell. In the dashed box, a zoom of the unit cell defining the TCAD simulation grid.
polarizations, are used to set the boundary condition for the raytracer at the front and rear interface of the c-Si substrate. As a result, the simulation provides the 2D (or 3D) map of the optical generation rate across the device, included the absorbing thin films simulated by TMM. The obtained photogeneration rate distribution is then averaged to define a 1D profile given as input to the classical Poisson-drift-diffusion (DD) transport model.

Fig. 2 reports the wavelength-dependent complex refractive index for the optical simulation, while the main material parameters for the electrical simulation are summarized in Table 1. The perovskite material is modeled as a classical inorganic semiconductor, in agreement with previous works in literature. As for recombination mechanisms, the model includes Shockely-Read-Hall recombination in the c-Si substrate only, with $e/h$ time constants of 1 ms; Auger coefficients in silicon are modeled following. Finally, the radiative recombination coefficient is set to: $1 \times 10^{-15}$ cm$^3$/s for a-Si:H, $4.73 \times 10^{-15}$ cm$^3$/s for c-Si, and $8 \times 10^{-10}$ cm$^3$/s for all the materials of the PVK subcell.

Fig. 3 compares the simulated External Quantum Efficiency (EQE) spectrum and the current-voltage ($J - V$) characteristics of the PVK/HIT-silicon tandem with the experimental ones reported in, showing good agreement. The presented simulations have been done in 2D, simulating in fact a triangular texture. This might be the main reason for the slightly lower calculated EQE of the HIT-silicon sub-cell with respect to the measured one. On the other hand, the mismatch in the PVK wavelength range could be attributed to some inaccuracy in the assumed geometrical features and to some uncertainty in the material parameters, such as the extinction coefficient of the perovskite material which is heavily dependent on the composition of the perovskite. The calculated

<table>
<thead>
<tr>
<th>PVK Top cell</th>
<th>HIT Bottom cell</th>
</tr>
</thead>
<tbody>
<tr>
<td>$E_g$ [eV]</td>
<td>2.95$^{10}$</td>
</tr>
<tr>
<td>$q\chi$ [eV]</td>
<td>2.18</td>
</tr>
<tr>
<td>$\epsilon_r$</td>
<td>3</td>
</tr>
<tr>
<td>$N_e$ [cm$^{-3}$]</td>
<td>$2.2 \times 10^{18}$</td>
</tr>
<tr>
<td>$N_h$ [cm$^{-3}$]</td>
<td>$1.8 \times 10^{19}$</td>
</tr>
<tr>
<td>$\mu_{[e]}$ [cm$^2$/Vas]</td>
<td>0.0002$^{14}$</td>
</tr>
<tr>
<td>$\mu_{[h]}$ [cm$^2$/Vas]</td>
<td>0.0002$^{14}$</td>
</tr>
<tr>
<td>$N$ [cm$^{-3}$]</td>
<td>$-5 \times 10^{18}$</td>
</tr>
</tbody>
</table>

Table 1. Main material parameters. $E_g$: bandgap; $\chi$: electron affinity; $\epsilon_r$: relative permittivity; $N_e$ ($N_h$): effective density of states in conduction (valence) band; $\mu$: mobility; $N$: net doping; [$e/h$]: electron/hole; if not specified, the value shown applies to both carriers.
short circuit current density \( (J_{sc}) \) for PVK and HIT-silicon subcell results as 21.1 and 18.5 mA/cm\(^2\), respectively, against the measured ones of 20.1 and 20.3 mA/cm\(^2\). The calculated open circuit voltage \( (V_{oc}) \) of the standalone PVK and HIT cells results as \( \approx 1.04 \) V and \( \approx 0.71 \) V, respectively, well matched to the experimental values. The tandem cell reaches a total \( V_{oc} \) of \( \approx 1.75 \) V, about 30 mV lower than the measured one, yielding an estimated tandem efficiency of 28.1%. From the comparison in Fig. 3, it is apparent that the simulated fill factor (FF) is significantly higher than the measured one. This stems from the fact that the model neglects any enhanced recombination at the textured surfaces as well as defect assisted recombination in the thin film layers.

3. PERFORMANCE OF THE PEROVSKITE/SILICON 3T-HBT SOLAR CELL

The PVK/Silicon 3T-HBT solar cell is developed based on the \( n-p-n \) structure of a bipolar junction transistor, whose regions are named as emitter, base and collector. The multilayer stack of our proposed PVK/HIT 3T-HBT tandem is sketched in Fig. 4. It consists of two sub-cells, i.e. the \( n-i-p \) emitter-base (EB) and the \( p-n \) base-collector (BC), that are electrically connected through the common base layer. In particular, the SnO\(_2\), C\(_{60}\), and PVK layers form the \( n \)-doped emitter region, the Spiro-TTB and \( p-(i) \) a-Si:H layers form the \( p \)-doped base region and finally the c-Si and \( (i)-(n) \) a-Si:H layers form the \( n \)-doped collector region. Compared to the 2T double junction (DJ) solar cell studied in the previous section, the 3T-HBT tandem is obtained by removing the tunnel junction, flipping the \( n-p \) HIT that is now illuminated from the \( p \) side, and implementing the third contact at the base region. The lateral base contact is implemented at the Spiro-TTB layer.

While the DJ architecture is characterized by two sub-cells interconnected in series and only one external load, in the 3T-HBTsc we can detect two back-to-back PVK/HIT sub-cells whose generated electrical power is extracted at two independent loads, relaxing the current matching constraint. The multilayer stack is simpler than that one of the DJ tandem because the HBT architecture avoids the use of tunnel or recombination junction. However, the lateral base contact will require the development of ad hoc solutions able to minimize shadow loss and parasitic resistances.

The 3T-HBTsc of Fig.4 is simulated in Sentaurus TCAD with the same optical and electrical model described in Sec. 2. Fig.5 shows the simulated EQE and \( J-V \) characteristic. The EQE of the HBT solar cell is almost identical to that one of the DJ tandem, because the high diffusion length of the HIT cell ensures a high collection efficiency regardless of the illuminated side, and the PVK cell is unchanged. If the PVK cell had been flipped, to obtain a \( p-n-p \) bipolar device, the optical response would be markedly different, due to the parasitic absorption of the hole transport layer and would have required a re-optimization of the design. Concerning the photovoltaic performance, it is worth noticing that the high bandgap and doping of the \( p \)-doped Spiro-TTB and a-Si:H
layers prevent any cross-talk, or transistor action, between the emitter and collector region; this implies that the current-voltage characteristic of the bottom BC cell is invariant with respect to the bias condition of the top EB cell (at least up to the maximum power point) and conversely for the EB cell with respect to the operating voltage of the BC one. Therefore, the individual $J - V$ characteristics shown in Fig. 5, are representative of the photovoltaic operation of the HBT tandem, which achieves a cumulative power conversion efficiency of $\sim 28.2\%$ with the two EB and BC sub-cells at maximum power point (0.89 V, 0.56 V), in line with that one of the baseline 2T tandem.

4. CONCLUSIONS

We have reported a preliminary simulation study of perovskite/silicon three-terminal tandem solar cells based on the heterojunction bipolar transistor architecture. Results show that this novel architecture can realistically achieve the same performance of the corresponding two terminal tandem with the advantage of a simpler multilayer stack, since no tunnel junction or recombination layer are needed. The bipolar transistor architecture is
suitable for the development of perovskite/silicon cells compatible with heterojunction and homojunction c-Si technologies which hold about 90% of the photovoltaic market. The promising performance of the intrinsic device prompt for further studies to devise novel grid architectures able to minimize shadow and resistive loss connected with the implementation of the extra base terminal.

REFERENCES


