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Digital-based Analog Processing in Nanoscale CMOS ICs for IoT Applications

Pedro Filipe Leite Correia de Toledo

Supervisors: Prof. Hamilton Klimach, Prof. Paolo Stefano Crovetto

Abstract

Over the past two decades, it is evident that there have been significant improvements (and it is expected to) in CMOS digital circuits when compared against analog building block performance. Digital circuits have been taking advantage of CMOS technology scaling in terms of speed, power consumption, and cost, while the techniques running behind the analog signal processing are still lagging. There has been an increasing trend in finding alternative IC design strategies to implement analog functions exploiting digital-in-concept design methodologies to decrease this historical gap. This idea of re-thinking analog functions in digital terms has shown that Analog ICs blocks can also avail of the feature-size shrinking and energy efficiency of new technologies. This poster shows the advanced of this field in the above scenario, proposing new digital-based analog blocks and proving their performance through silicon measurements.

Novel contributions

- Novel Ultra Low Voltage (ULV) Digital-Based (DB) analog blocks, such as Operational Transconductance Amplifier, are proposed.

Research Collaborations

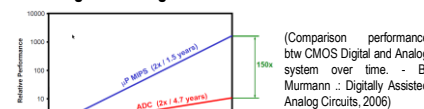


Joint Degree PhD

Research context and motivation

- IoT Application and Low Power requirement:

- Analog versus Digital:

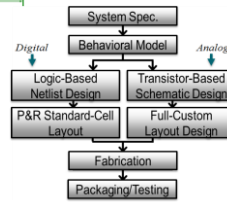


Analog Design Flow:

- Longest design time
- Manual layout
- Labor intensive
- Highest performance

Digital Design Flow:

- Automatic
- Portability
- Time-to-market
- Reasonable performance

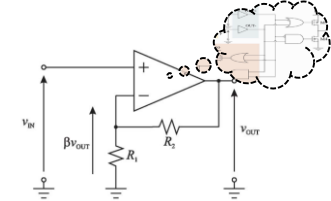
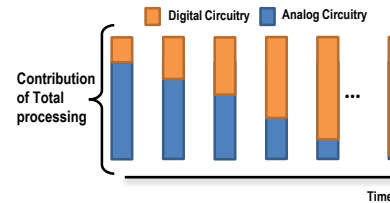


Submitted/published works and Awards

- [1] P. S. Crovetto, F. Musolino, O. Aiello, P. Toledo, and R. Rubino, "Breaking the boundaries between analog and digital," *Electronics Letters*, vol. 55, no. 12, pp. 672-673, 13 6 2019.
- [2] P. Toledo, et al., "A 300mV-Supply, 2mW-Power, 80pF-Leak-CMOS Digital-Based OTA for IoT Interfacing," *International Conference on Electronics Circuits and Systems*, 2019 [best student paper]
- [3] P. Toledo, O. Aiello, P. Crovetto, "A 300mV-Supply Standard-Cell-Based OTA with Digital PWM Offset Calibration," *IEEE Nordic Circuits and Systems Conference*, 2019
- [4] P. Toledo, Crovetto, P., Klimach, H., Bampi, S., "Dynamic and Static Calibration of Ultra-Low-Voltage, Digital-Based Operational Transconductance Amplifiers," *Electronics* 2020
- [5] P. Toledo, Crovetto, P., Aiello, O., Aiello, M., "Fully-Digital Rail-to-Rail OTA with Sub-1,000 μm^2 Area, 250-mV Minimum Supply and nW Power at 150-pF Load in 180nm" *Solid State Circuit Letters*, 2020
- [6] Aiello, O., Crovetto, P., Toledo, P., Aiello, M., "Rail-to-Rail Dynamic Voltage Comparator Scalable down to pW-Range Power and 1.5V Supply" *Transactions on Circuits and Systems II*, 2020 (submitted)
- [7] Demo video link: <https://www.dropbox.com/s/1kqg7yayw6m5264711/Poster%20demo.mp4?dl=0> and <https://www.youtube.com/watch?v=2mJ8Q4v4Y>

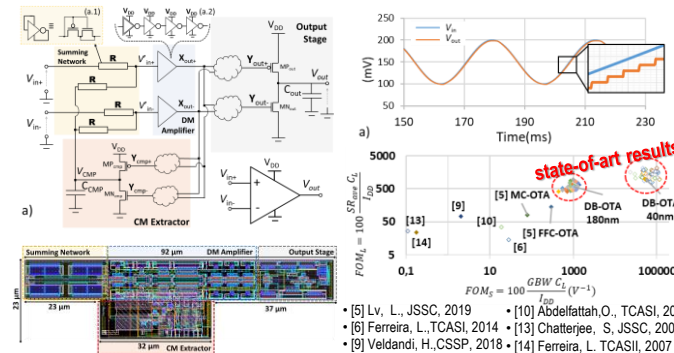
Addressed research questions/problems

- There has been an increasing trend in finding alternative IC design strategies to implement analog functions using digital-in-concept design.
- Can we design OTAs and Comparator using only digital logic gates targeting Ultra Low Voltage applications?

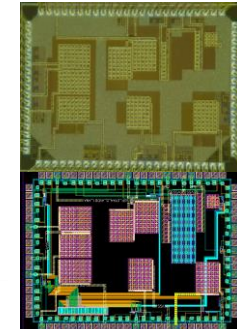


Adopted methodologies

- In [2], an 180nm ULV DB-OTA has been proposed following digital design criteria

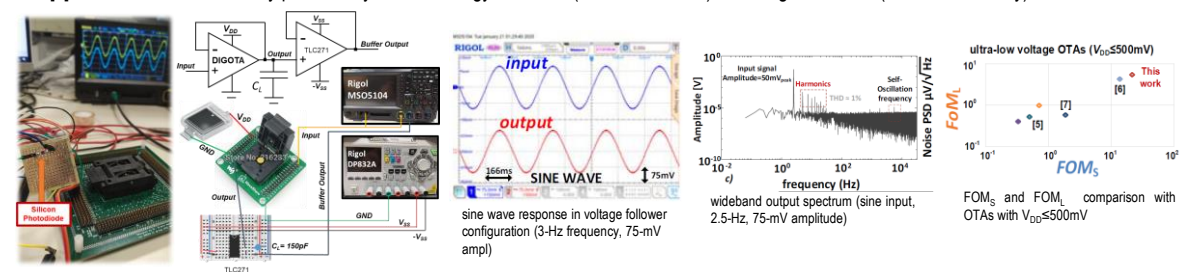


- Based on [2], several others ULV Digital-based analog blocks have been fabricated in TSMC 180nm



- Total Area: 1500 $\mu\text{m} \times 2000 \mu\text{m}$
- TSMC 180 nm
- 82 PADS
- 2 New DB-OTA
- 2 New DB-Comparator
- 1 New DB-Oscillator
- 1 DB-Filter

- In [7], a new DIGOTA is directly powered by a small energy harvester (7-mm² solar cell) at dim light <100 lux (dark overcast day).



Conclusion

- Several ULV digital-based analog building blocks have been proposed.
- Development of a semi-automatic testbench is herein reported.
- A compact and energy-efficient digital OTA has been in 180 nm and its ability to operate at ultra-low voltage and power has been demonstrated in the context of energy-autonomous sensor nodes, as directly powered by a small energy harvester.