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On Partial Response Signaling for MIMO Equalization on Multi-Gbit/s Electrical Interconnects

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Abstract—Because of its ability to deal with intersymbol interference (ISI) and crosstalk (XT) over mutually coupled electrical interconnects, multiple-input multiple-output (MIMO) decision feedback equalization (DFE) has proven to be a promising low-cost solution for achieving multi-Gbit/s wireline communication on- and off-chip. However, not only does the channel become very sensitive to manufacturing tolerances at very high symbol rates, the latency in the feedback loop becomes prohibitively large as well. Whereas the former issue has been addressed by adopting a stochastic MIMO approach where (part of) the equalization filters depend on the channel statistics rather than on the actual channel, we tackle in this paper the latency issue by setting to zero the first N taps of the feedback filters. Moreover, we show that precoded partial response (PR) signaling can improve the performance of the resulting scheme, although the achieved gain is smaller than in the case of single-input single-output (SISO) equalization.

I. INTRODUCTION

In order to enable high-speed communication over low-cost parallel electrical interconnects on- and off-chip, many state-of-the-art transceivers deal with the intersymbol interference (ISI) by applying either linear equalization at the transmitter (TX) and/or receiver (RX) or non-linear equalization by means of Tomlinson-Harashima precoding (THP) at the TX or decision feedback equalization (DFE) at the RX [1]–[5]. Moreover, it has been shown that multiple-input multiple-output (MIMO) equalization is able to significantly outperform single-input single-output (SISO) equalization, as it allows to exploit the useful information in the crosstalk (XT) signals [6].

With growing symbol rates, two complications are emerging: i) the channel becomes very sensitive to manufacturing tolerances, so that every interconnect has a (slightly) different impulse response, and ii) the latency in the feedback loop becomes prohibitively large. In [7], the former issue was addressed by adopting a stochastic MIMO DFE approach where (part of)

the equalization filters depend on the channel statistics rather than on the actual channel, which allows to significantly reduce the computational and implementational complexity. In this contribution, we tackle the latency issue by setting to zero the first N taps of the feedback filters of a MIMO DFE scheme consisting of a fixed pre-equalizer at the TX and adjustable feedforward and feedback filters at the RX. Moreover, the resulting scheme is combined with partial response (PR) signaling, which has been shown to improve the performance of high-speed serial links because it allows some controlled residual ISI and, hence, reduces the required equalization effort [8], [9]. We derive neat matrix expressions for the minimum mean-square error (MMSE) equalization filters and show how precoded PR signaling on a specific 4×4 chip-to-chip interconnect can enhance the error performance of MIMO DFE.

In the following, $(\cdot)^T$, $\text{tr}(\cdot)$, $\|\cdot\|$, and $\mathbb{E}[\cdot]$ denote transposition, the trace, the Frobenius norm, and statistical expectation, respectively.

II. SYSTEM MODEL

We consider the baseband MIMO communication system from Fig. 1. The system's input is given by the L -dimensional sequence $\{\mathbf{c}(k)\}$, representing L mutually independent i.i.d. sequences of M -ary digits, each uniformly distributed over the set $\{0, 1, \dots, M-1\}$ and having a rate $1/T$. Since M is assumed to be an integer power of 2, an M -ary digit represents $\log_2(M)$ bits. The PR precoder, characterized by the polynomial $h_{\text{poly}}(D) = 1 + \sum_{m=1}^{L_t} h_t(m)D^m$ with integer coefficients $h_t(m)$, converts the input sequence $\{\mathbf{c}(k)\}$ into the precoded sequence $\{\mathbf{b}(k)\}$, according to

$$\mathbf{b}(k) = \left[\mathbf{c}(k) - \sum_{m=1}^{L_t} h_t(m)\mathbf{b}(k-m) \right]_M \quad (1)$$

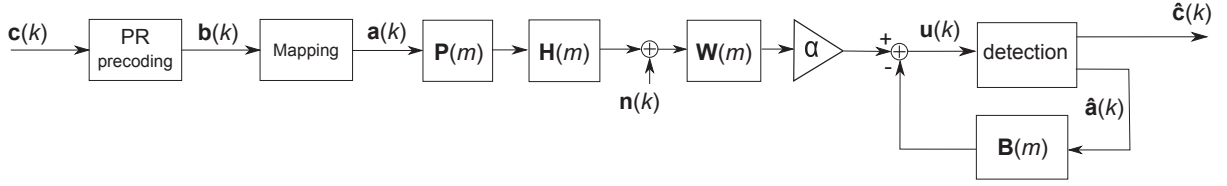


Figure 1. MIMO baseband communication scheme with equalization at both the TX and the RX.

where $[\mathbf{x}]_M$ denotes the modulo- M reduction of each element of \mathbf{x} to the set $\{0, 1, \dots, M-1\}$. The resulting sequence $\{\mathbf{b}(k)\}$, which consists of L mutually independent sequences of temporally correlated M -ary digits, is mapped to a data symbol sequence $\{\mathbf{a}(k)\}$ according to $\mathbf{a}(k) = 2\mathbf{b}(k) - M + 1$, so that all symbols belong to the M -PAM constellation $\{-(M-1), -(M-3), \dots, M-3, M-1\}$; the symbol variance is given by $\sigma_a^2 = \frac{M^2-1}{3}$. Before being transmitted on the channel, the data symbols are applied to the MIMO pre-equalization filter, with impulse response matrix $\mathbf{P}(m)$. The received signal, affected by the MIMO channel with impulse response matrix $\mathbf{H}(m)$ and the additive noise $\mathbf{n}(k)$, is further equalized by MIMO feedforward and feedback equalization filters, with impulse response matrices $\mathbf{W}(m)$ and $\mathbf{B}(m)$, respectively. The matrices $\mathbf{P}(m)$, $\mathbf{H}(m)$, $\mathbf{W}(m)$ and $\mathbf{B}(m)$ all have size $L \times L$. The scaling factor α is adjusted according to the transmit energy, given a certain feedforward filter $\mathbf{W}(m)$. Obviously, the equalization filters must be selected such that the vector $\mathbf{u}(k)$ of decision variables approaches the PR target response $\mathbf{u}_t(k) = \sum_{m=0}^{L_t} h_t(m)\mathbf{a}(k-m)$, with $h_t(0) = 1$, since the original M -ary digits $\mathbf{c}(k)$ can be recovered from $\mathbf{u}_t(k)$ as

$$\mathbf{c}(k) = \frac{1}{2} \left[\mathbf{u}_t(k) + (M-1) \sum_{m=0}^{L_t} h_t(m) \right]_{2M} \quad (2)$$

The decisions $\hat{\mathbf{c}}(k)$ are obtained by replacing $\mathbf{u}_t(k)$ by $\mathbf{u}(k)$ in (2); the decisions $\hat{\mathbf{a}}(k)$ at the input of the feedback filter result from applying to $\hat{\mathbf{c}}(k)$ the same precoding and mapping as at the TX. Assuming the decisions $\hat{\mathbf{a}}(k)$ are correct, it was shown in [7] that the decision variables $\mathbf{u}(k)$ are given by

$$\mathbf{u}(k) = \alpha \left[\sum_{m \in \Psi_G} \mathbf{W}\mathbf{G}(m)\mathbf{P} \mathbf{a}(k-m) + \mathbf{W}\check{\mathbf{n}}(k) - \sum_{m \in \Psi_b} \mathbf{B}(m) \mathbf{a}(k-m) \right] \quad (3)$$

where the (LL_w) -dimensional stacked column vector $\check{\mathbf{n}}(k)$ is given by

$$\check{\mathbf{n}}(k) = \begin{bmatrix} \mathbf{n}(k + L_w^1) \\ \vdots \\ \mathbf{n}(k - L_w^2) \end{bmatrix}.$$

The equalization filters in (3) are assumed to be FIR filters with a limited number of taps. The $(LL_p) \times L$ pre-equalizer filter matrix \mathbf{P} is defined as:

$$\mathbf{P} = \begin{bmatrix} \mathbf{P}(-L_p^1) \\ \vdots \\ \mathbf{P}(L_p^2) \end{bmatrix}. \quad (4)$$

Likewise, at the RX we have the $L \times (LL_w)$ feedforward filter matrix

$$\mathbf{W} = [\mathbf{W}(-L_w^1), \dots, \mathbf{W}(L_w^2)], \quad (5)$$

and the $L \times LL_b$ feedback filter matrix

$$\mathbf{B} = [\mathbf{B}(L_b^1), \dots, \mathbf{B}(L_b^2)]. \quad (6)$$

Note that we have $L_p = L_p^1 + L_p^2 + 1$ taps per pre-equalizer, $L_w = L_w^1 + L_w^2 + 1$ taps per feedforward filter, and $L_b = L_b^2 - L_b^1 + 1$ taps per feedback filter. Conventionally, one takes $L_b^1 = 1$, but in order to relax the latency constraints on the device which computes the decisions on the M -PAM data symbols, we also consider $L_b^1 = N + 1$ with $N > 0$. The elements of the impulse response matrix $\mathbf{H}(m)$ are arranged into the $(LL_w) \times (LL_p)$ block matrices $\mathbf{G}(m)$:

$$\mathbf{G}(m) = \begin{bmatrix} \mathbf{G}_m(-L_w^1, -L_p^1) & \dots & \mathbf{G}_m(-L_w^1, L_p^2) \\ \vdots & & \vdots \\ \mathbf{G}_m(L_w^2, -L_p^1) & \dots & \mathbf{G}_m(L_w^2, L_p^2) \end{bmatrix}, \quad (7)$$

where the $L \times L$ matrix $\mathbf{G}_m(m_1, m_2)$ is given by

$$\mathbf{G}_m(m_1, m_2) = \mathbf{H}(m - m_1 - m_2). \quad (8)$$

Note that the impulse response matrix $\mathbf{H}(m)$ is obtained by sampling at instants $\{mT + \tau\}$ the continuous-time response $\mathbf{H}_{\text{tot}}(t)$ which is given by the convolution of $h_{\text{tr}}(t)$, $\mathbf{H}_{\text{ch}}(t)$, and $h_{\text{rec}}(t)$; $h_{\text{tr}}(t)$ and $h_{\text{rec}}(t)$ are the impulse responses of the continuous-time transmit and receive filters, respectively, whereas $\mathbf{H}_{\text{ch}}(t)$ denotes the impulse response matrix of the MIMO channel. The elements of $\mathbf{H}(m)$ are assumed to have limited time duration, so that the matrices $\mathbf{G}(m)$ are considered zero for m outside the interval $\Psi_G = [-L_G^1, L_G^2]$.

III. MMSE MIMO DFE

Since the M -ary digits $\mathbf{c}(k)$ follow from the target response $\mathbf{u}_t(k)$ according to (2), we define the mean

square error (MSE), conditioned on the MIMO channel \mathbf{G} , as follows:

$$\text{MSE}_{\mathbf{G}} \triangleq \frac{\mathbb{E} \left[\|\mathbf{e}(k)\|^2 \right]}{\mathbb{E} \left[\|\mathbf{a}(k)\|^2 \right]}, \quad (9)$$

where $\mathbf{e}(k)$ is the error vector $\mathbf{e}(k) = \mathbf{u}(k) - \mathbf{u}_t(k)$. Taking (3) into account, and assuming that $h_t(m) = 0$ for $m \notin \Psi_t$, with $\Psi_t = [0, L_t]$, $\mathbf{e}(k)$ can be written as

$$\begin{aligned} \mathbf{e}(k) &= \sum_{m \in \Psi_0} (\alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - h_t(m) \mathbf{I}_L) \mathbf{a}(k-m) \\ &+ \sum_{m \in \Psi_b} (\alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - \mathbf{B}(m) - h_t(m) \mathbf{I}_L) \mathbf{a}(k-m) \\ &+ \alpha \mathbf{W} \check{\mathbf{n}}(k), \end{aligned} \quad (10)$$

where $\Psi_0 = \Psi_G \setminus \Psi_b$ is the complement of Ψ_b in Ψ_G , with $\Psi_b = [L_b^1, L_b^2]$. It follows from (10) that the conditional MSE (9), conditioned on the MIMO channel \mathbf{G} , can be written as

$$\begin{aligned} \text{MSE}_{\mathbf{G}} &= \frac{1}{L} \left[\sum_{m \in \Psi_0} \|\alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - h_t(m) \mathbf{I}_L\|^2 \right. \\ &+ \sum_{m \in \Psi_b} \|\alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - \mathbf{B}(m) - h_t(m) \mathbf{I}_L\|^2 \\ &\left. + \frac{\alpha^2}{\sigma_a^2} \text{tr}(\mathbf{W} \mathbf{R}_n \mathbf{W}^T) \right], \end{aligned} \quad (11)$$

which for given \mathbf{G} is a function of $(\mathbf{P}, \mathbf{W}, \alpha, \mathbf{B})$. In (11), the $LL_w \times LL_w$ autocorrelation matrix \mathbf{R}_n is defined as

$$\mathbf{R}_n \triangleq \mathbb{E} [\check{\mathbf{n}}(k) \check{\mathbf{n}}(k)^T]. \quad (12)$$

The pre-equalization matrix \mathbf{P} needs to be selected such that the average TX energy per symbol interval is limited to LE_s , yielding the following energy constraint:

$$\sigma_a^2 \text{tr}(\mathbf{P}^T \mathbf{G}_{\text{tr}} \mathbf{P}) = LE_s, \quad (13)$$

where the elements of the $(LL_p) \times (LL_p)$ matrix \mathbf{G}_{tr} are given by

$$(\mathbf{G}_{\text{tr}})_{n_1, n_2} = \int_{-\infty}^{+\infty} h_{\text{tr}}(t) h_{\text{tr}}(t + (n_1 - n_2) T_p) dt. \quad (14)$$

Since the conditional MSE (11) is a function of the actual channel, the MMSE equalization filters are obtained by minimizing the average MSE, which is given by

$$\text{MSE}_{\text{avg}} = \mathbb{E}_{\mathbf{G}} [\text{MSE}_{\mathbf{G}}] \quad (15)$$

where $\mathbb{E}_{\mathbf{G}} [\cdot]$ denotes expectation over \mathbf{G} .

The MIMO equalization scheme from Fig. 1 is assumed to be a hybrid scheme consisting of a fixed pre-equalization filter \mathbf{P} , and adjustable feedforward and feedback filters \mathbf{W} and \mathbf{B} . In this way, no channel state information must be passed from the RX to the TX. For each channel realization \mathbf{G} , the optimal

equalizer $(\mathbf{P}_h, \alpha_h, \mathbf{W}_h, \mathbf{B}_h)$, consisting of the fixed (\mathbf{P}_h, α_h) and the adjustable $(\mathbf{W}_h, \mathbf{B}_h)$, minimizes the average MSE (15) under the energy constraint (13).

a) Feedback filter: For a given fixed pre-equalization matrix \mathbf{P} and scaling factor α , and a given adjustable feedforward filter matrix \mathbf{W} , the optimal adjustable feedback filter minimizes $\text{MSE}_{\mathbf{G}}$ from (11), yielding:

$$\mathbf{B}_h(m) = \alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - h_t(m) \mathbf{I}_L. \quad (16)$$

for $m \in \Psi_b$.

b) Feedforward filter: When (16) holds, the conditional MSE (11) reduces to

$$\begin{aligned} \text{MSE}_{\mathbf{G}} &= \frac{1}{L} \left[\sum_{m \in \Psi_0} \|\alpha \mathbf{W} \mathbf{G}(m) \mathbf{P} - h_t(m) \mathbf{I}_L\|^2 \right. \\ &\left. + \frac{\alpha^2}{\sigma_a^2} \text{tr}(\mathbf{W} \mathbf{R}_n \mathbf{W}^T) \right]. \end{aligned} \quad (17)$$

Given a fixed pre-equalization matrix \mathbf{P} and scaling factor α , the MMSE feedforward matrix \mathbf{W}_h minimizes the conditional MSE (17), yielding:

$$\mathbf{W}_h = \frac{1}{\alpha} \mathbf{P}^T \left(\sum_{m \in \Psi_t} h_t(m) \mathbf{G}(m)^T \right) \mathbf{C}_h^{-1}, \quad (18)$$

with

$$\mathbf{C}_h \triangleq \sum_{m \in \Psi_0} \mathbf{G}(m) \mathbf{P} \mathbf{P}^T \mathbf{G}(m)^T + \frac{\mathbf{R}_n}{\sigma_a^2}, \quad (19)$$

c) Pre-equalization filter and scaling factor:

Given the adjustable feedforward and feedback filters \mathbf{W} and \mathbf{B} , the fixed pre-equalization matrix \mathbf{P}_h and scaling factor α_h are obtained by minimizing, with respect to \mathbf{P} and α , the Lagrangian Λ , which is given by

$$\Lambda = \text{MSE}_{\text{avg}} + \lambda \frac{\sigma_a^2 \text{tr}(\mathbf{P}^T \mathbf{G}_{\text{tr}} \mathbf{P}) - LE_s}{L \sigma_a^2}, \quad (20)$$

where MSE_{avg} is the average MSE from (15) with $\text{MSE}_{\mathbf{G}}$ given by (17). Note that the expectation over \mathbf{G} in (15) now applies to both $\mathbf{G}(m)$ and \mathbf{W} , as the latter is a function of \mathbf{G} . Equating to zero the derivatives of Λ with respect to α and the components of \mathbf{P} , and taking into account the constraint (13), it can be shown that at the optimum we have $\lambda = \lambda_h$, with

$$\lambda_h = \alpha_h^2 \frac{\text{tr}(\mathbb{E}_{\mathbf{G}} [\mathbf{W} \mathbf{R}_n \mathbf{W}^T])}{LE_s}. \quad (21)$$

Substituting (21) into the derivatives of Λ and solving for (\mathbf{P}, α) yields

$$\mathbf{P}_h = \frac{1}{\alpha_h} \tilde{\mathbf{P}}_h, \quad (22)$$

with

$$\tilde{\mathbf{P}}_h = \mathbf{D}_h^{-1} \mathbb{E}_{\mathbf{G}} \left[\left(\sum_{m \in \Psi_t} h_t(m) \mathbf{G}(m)^T \right) \mathbf{W}^T \right], \quad (23)$$

Table I
GEOMETRICAL AND MATERIAL PARAMETERS

| | |
|---|-------------------|
| Gap between two conductors | 80 μm |
| Width of signal conductor | 100 μm |
| Thickness of signal conductor | 35 μm |
| Conductivity of signal conductor | 58 MS/m |
| Thickness of dielectric substrate | 500 μm |
| ε_r of dielectric substrate | 4 |
| Dielectric loss ($\tan \delta$) | 0.02 |

$$\mathbf{D}_h \triangleq \sum_{m \in \Psi_0} \mathbb{E}_{\mathbf{G}} [\mathbf{G}(m)^T \mathbf{W}^T \mathbf{W} \mathbf{G}(m)] + \frac{\text{tr}(\mathbb{E}_{\mathbf{G}} [\mathbf{W} \mathbf{R}_n \mathbf{W}^T])}{LE_s} \mathbf{G}_{\text{tr}}. \quad (24)$$

and

$$\alpha_h = \sqrt{\frac{\sigma_a^2}{LE_s} \text{tr}(\tilde{\mathbf{P}}_h^T \mathbf{G}_{\text{tr}} \tilde{\mathbf{P}}_h)}. \quad (25)$$

In practice, we assume that we have access to a set of J independent channel realizations \mathbf{G}_j , with $1 \leq j \leq J$. From this set, the optimal fixed pre-equalizer and scaling factor (\mathbf{P}_h, α_h) can be obtained offline using an iterative approach, where during the i -th iteration the pair $(\mathbf{P}_h^{(i)}, \alpha_h^{(i)})$ is obtained by replacing the statistical averages $\mathbb{E}_{\mathbf{G}}[\cdot]$ in (23) and (24) by arithmetical averages using the pairs $(\mathbf{G}_j(m), \mathbf{W}_{h,j}^{(i-1)})$, with $1 \leq j \leq J$. Hence, during the i -th iteration, all forward filters $\mathbf{W}_{h,j}^{(i)}$, with $1 \leq j \leq J$, must be calculated using (18) and (19), with (\mathbf{P}_h, α_h) and \mathbf{G} replaced by $(\mathbf{P}_h^{(i)}, \alpha_h^{(i)})$ and \mathbf{G}_j . Once the fixed (\mathbf{P}_h, α_h) are known, the adjustable filters \mathbf{B}_h and \mathbf{W}_h are obtained for the actual channel \mathbf{G} from (16) and (18), respectively.

IV. NUMERICAL RESULTS

In this section, we apply the proposed equalization strategy to a simulated electrical chip-to-chip interconnect consisting of four parallel chip-to-chip interconnects (microstrip lines). The nominal values of the geometrical and material parameters are given in Table I. Because of tolerances during the manufacturing process, the listed parameters are random variables (RVs) with a mean equal to the corresponding nominal value and a standard deviation σ_r , expressed as a percentage of the mean. As the sum of the conductor gap and the conductor width is considered to be constant, only the last six of the listed parameters are varying independently. By gathering the latter parameters in the vector ϕ , and by defining the manufacturing tolerance (MT) as three times the standard deviation σ_r , nearly all (i.e., $(99.7\%)^6 = 98.4\%$) realizations of ϕ are within the range of $(1 \pm \text{MT})\boldsymbol{\mu}$, where $\boldsymbol{\mu} = \mathbb{E}[\phi]$ is the mean of ϕ .

In our examples, we consider a standard deviation σ_r of 3%, corresponding to a 9% MT. Using the polynomial chaos-based approach outlined in [10],

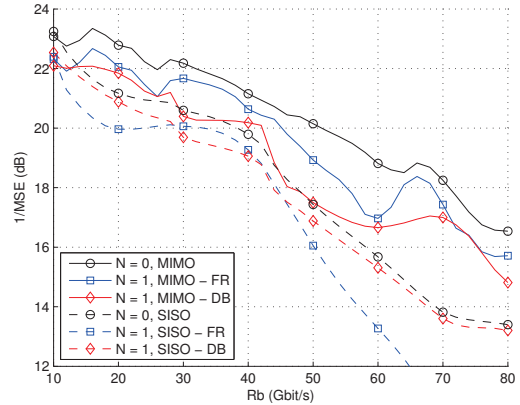


Figure 2. $1/\text{MSE}_{\text{avg}}$ of the hybrid scheme as a function of bit rate ($E_s/N_0 = 27$ dB).

[11], a data set consisting of $J = 200$ realizations of $\mathbf{H}_{\text{ch}}(t)$ is generated. The transmit and receive filters are assumed to be unit-energy square-root raised cosine filters having a 3 dB bandwidth of $1/2T$ and a roll-off factor $\beta = 0.2$. We use 50 iterations to obtain the fixed (\mathbf{P}_h, α_h) .

For a bit rate ranging from $R_b = 10$ Gbit/s to $R_b = 80$ Gbit/s per lane, we display in Fig. 2 $1/\text{MSE}$ for several SISO and MIMO equalization schemes. As we intend to compare the impact of PR signaling on MIMO and SISO links, we set the XT channels to zero in the SISO case. The signal-to-noise ratio (SNR) is given by $E_s/N_0 = 27$ dB, with $N_0/2$ being the variance of the i.i.d. Gaussian noise samples. We consider both full response (FR) and duobinary (DB) signaling, characterized by the polynomials $h_{\text{poly}}(D) = 1$ and $h_{\text{poly}}(D) = 1 + D$, respectively. Both the pre-equalization filters and the feedforward filters consist of 5 equalization coefficients ($L_p^1 = L_p^2 = 2$, $L_w^1 = L_w^2 = 2$). The number of feedback filter taps is either $L_b = 100$ ($L_b^1 = 1$, $L_b^2 = 100$) or $L_b = 99$ ($L_b^1 = 2$, $L_b^2 = 100$). In the former case, where $L_b^1 = 1$ ($N = 0$), FR and DB signaling yield the same minimum MSE, as it follows from (11) that the coefficients $h_t(m)$, with $m \in \Psi_b$, can be fully compensated for by the feedback filter \mathbf{B} . However, it follows from the figure that relaxing the latency constraints on the feedback loop by taking $L_b^1 = 2$ ($N = 1$) introduces a significant performance degradation in the SISO FR case, as the ISI term for $m = 1$ must now be totally mitigated by the linear equalization filters, resulting in increased noise enhancement. For DB signaling, on the other hand, the performance degradation associated with taking $L_b^1 = 2$ can be kept to a minimum as the controlled ISI term for $m = 1$ reduces the required equalization effort. In the case of MIMO equalization, the impact of the signaling scheme on the MSE is slightly more complex. Since DB signals have a narrower bandwidth than their FR counterparts, the latter tend to generate

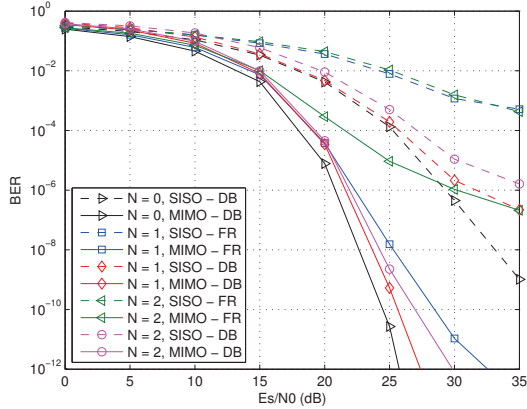


Figure 3. BER of the hybrid scheme as a function of SNR for 2-PAM ($R_b = 70$ Gbit/s).

more XT, which can be put to good use by the MIMO equalization scheme. Hence, both signaling formats have their (dis)advantages and the specific transmission parameters and channel will determine whether FR or DB signaling is to be preferred.

Assuming a 2-PAM constellation and a bit rate of $R_b = 70$ Gbit/s per lane, Fig. 3 displays the BER versus the SNR for FR and DB signaling, with $0 \leq N \leq 2$. For $N = 0$, only the BER for DB signaling is shown, since the BER curves for FR and DB practically coincide (note that the DB scheme suffers from modulo loss due to the modulo operation at the RX). For $N > 0$, DB is shown to significantly outperform FR, for both SISO and MIMO equalization. At a target BER of 10^{-12} , the MIMO DB scheme experiences a degradation of about 2 dB ($N = 1$) and 4 dB ($N = 2$), with respect to the case where $N = 0$, whereas for the FR scheme, we observe a degradation of about 7 dB ($N = 1$) and an error floor ($N = 2$). Since the large number of feedback filter coefficients may involve a prohibitively high complexity, we have also obtained the BER for an alternative scheme where we keep only the 20 dominant filter coefficients (located around the main peak and the first reflection of the channel impulse response) and set the others to zero. As the latter results did not yield any visual degradation of the BER, which is in line with the results from [6], they are not included in the figure,

V. CONCLUSIONS

In this work, we extended the stochastic MIMO DFE approach from [7] by setting to zero the first N taps of the feedback filters in order to relax the latency constraints on the feedback loop. PR signaling was shown to potentially enhance the performance of MIMO DFE for $N > 0$, by reducing the required equalization effort. However, as PR signals tend to have a narrower bandwidth and therefore generate less XT than their FR counterparts, MIMO equalization

schemes can benefit less from exploiting the useful information in the XT signals. Nevertheless, at a target BER of 10^{-12} , duobinary signaling was shown to provide significantly better BER performance than FR signaling on a typical 4×4 chip-to-chip interconnect.

VI. ACKNOWLEDGEMENTS

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