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Article

PWM Carrier Displacement in Multi-N-Phase Drives: An Additional Degree of Freedom to Reduce the DC-Link Stress

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Abstract: The paper presents a particular Pulse Width Modulation (PWM) strategy to reduce the (Direct Current) DC-link capacitor stress for multi-n-phase drives. A multi-n-phase drive is composed of multiple independent systems of n inverter supplying a multi-n-phase electric machine. The paper focused on the investigation of the best phase shifting between carriers for a triple-3-phase drive compared to the 3-phase counterpart in order to reduce the capacitor bench design point. Simulation and experimental results show as the control technique proposed is able to reduce the value of the DC-link capacitor current in any operating condition including fault case. In this sense, the PWM carrier displacement appears like an additional degree of freedom that can be exploited in multi-n-phase drives but also in multi-motor application.

Keywords: DC-link stress; multiphase drives; pulse width modulation; motor drives; power capacitors; fault tolerance; electric vehicles

1. Introduction

In the automotive field, the reasons to search for integrated motor drive solutions are the same that pushed, some years ago, at looking for integrated plug and play solutions in the industrial environment [1]. Integrated solutions of electric machines and power electronics could bring strong benefits: to reduce (or eliminate) the cable length between motor and inverter with advantages in terms of overvoltages on the motor side [2], to help to mitigate Electromagnetic Compatibility (EMC) and Electromagnetic Interference (EMI) problems, to obtain a better International Protection Rating (IP), to realize a shared cooling system, and more integrated solutions lead to a strong reduction in the required space for the whole traction drive.

A particular drive that allows for obtaining more insightful integrated solutions is the multi-phase one [3]. In fact, in multi-phase structures, it is possible to realize a converter as a combination of standard modules with an equal subdivision of the current. The resulting power electronics modules meet the needs of an integrated solution: smaller and widely distributed.

Although road electric vehicles primarily adopt three-phase drives, the multi-phase version could represent a good alternative not only for its integration capability but also for other features like reduced weight and volume, high efficiency, low vibrations and noise, robustness and, overall, fault tolerance [4–6].

Advantages of multi-phase motors have been well evidenced in the past [7–9], especially for the application in which the machine current is very large [10,11]. The interest for multi-phase drives in

the traction field has grown in recent years too [4,5,12,13]. One of the forms of multi-phase drives is the multi-n-phase, which is based on an electric machine constituted by multiple n-phase insulated stars [8].

The benefits offered by the power modules size reduction in the multi-n-phase machine has a cost: the converter structure becomes more complex. This intrinsic complexity can anyway be efficiently used to obtain further advantages. In a generic multi-phase machine, overall performance can be improved introducing additional degrees of freedom addressed as well in [7,14]. Improvements like torque enhancement and design for fault tolerant strategies can be obtained but cannot be exploited simultaneously.

The purpose of the paper is to investigate and test the benefits introduced from an additional degree of freedom (DoF) for the multi-n-phase electrical drives that does not preclude fault tolerance capability.

Multi-n-phase drives, in fact, offer the chance to use PWM carriers' phase displacement for the different converters to reduce the DC-link RMS (Root Mean Square) current [15]. In particular, the possibility to reduce the size of the DC-link capacitor bench adopting a multi-n-phase drive has been presented in [15], as a general approach and in [16] for a six-phase structure. In addition, the phase displacement between carriers has been introduced in the past in order to reduce the harmonic distortion and power losses [17,18] in six-phase drives or to improve the quality of the line-to-line output voltages in interleaved Voltage-Source Inverters [19].

This paper suggests a simple PWM technique for triple-3-phase drives and offers a detailed analysis of the effects, on the converter side, of the proposed PWM technique extending the work of [15]—all of this in order to reach conscious guidelines to define the DC-link design conditions.

The paper firstly gives a presentation of the main rules for the peculiar multi-n-phase motor structure adopted. In particular, a multi-3-phase drive and its 3-phase counterpart are presented with an overview on the differences between the two solutions. The PWM strategy and the related reduction of the DC-link stresses through the PWM carriers phase displacement is presented and simulation results are collected. The limitation of the DC-link rms current offered by the PWM phase shifting is studied throughout all the possible operating points also considering the fault conditions. The proposed simulation methodology is validated through two different test bench setups with the aim to offer guidance for the uses that we are aware of for the proposed control strategy and then for the correct design of the DC-link capacitor.

2. Drive Solutions

As multi-n-phase or multi-star drive, the multi-3-phase one is chosen. This extension of 3-phase based drives is very fitting considering the widespread availability of 3-phase converters both in the power and in the control area. A multi-phase converter, in fact, can be generally obtained as the combination of modular systems. The number of adopted modular systems in the final drive depends on the modules availability or by fault tolerant features required. In a multi-star drive, it is possible to increase the fault-tolerant features reducing the number of phases supplied by a single source [20]. The paper focuses on triple-3-phase motor supplied by three 3-phase inverters (9 legs in total).

2.1. The Multi-N-Phase Motor

A 10-pole permanent magnet surface mounted (PMSM) machine, 9-coils double layer concentrated windings can be considered. As a base machine, rotor and stator structures are reported in Figures 1a, 2a and 3a. With the same stator and rotor structures, two electric machine are possible:

- $q_{3/10}$ as a resulting 3-phase double layer machine, defined by the parameter q (number of slot-per-pole-per-phase) and by the number of permanent magnets (poles),
- 3-3-10-1, meaning three stars of three phases, 10 pole, repeated 1 times, in the multi-n-phase version.

This duality means that, with the stator and rotor structure proposed, it is possible to realize three different motor-converter configurations. Different winding arrangements and matched converters allow for obtaining the following drive solutions:

- I 3-phase machine $q_{3/10}$ powered by a single-3-phase converter as in Figure 1,
- II 3-phase machine $q_{3/10}$ powered by a triple-3-phase converter with coil connections as in Figure 2, the center taps N_a, N_b, N_c (as shown later in Figure 4) connect together,
- III triple-3-phase machine supplied by a triple-3-phase converter with coils connections as in Figure 3 and the center taps insulated from each other.

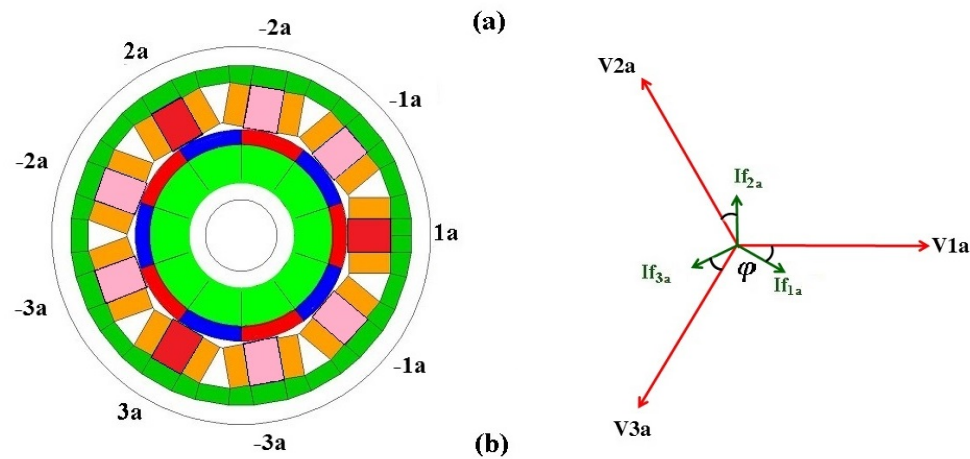


Figure 1. Case I: base machine (a) and phasor diagrams (b). Red stator teeth are the axes of the coil supplied by the converter a. φ is the V-I phase angle.

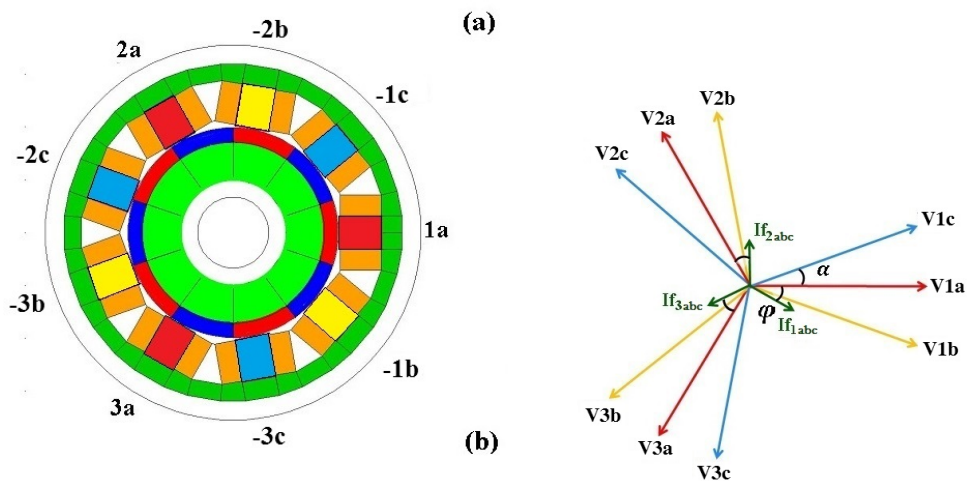


Figure 2. Case II: base machine (a) and phasor diagrams (b). Red stator teeth are supplied by the converter a, yellow teeth by the converter b; blue teeth by the converter c. φ is the V-I phase angle and α is the electric angle between coil supplied by different converter.

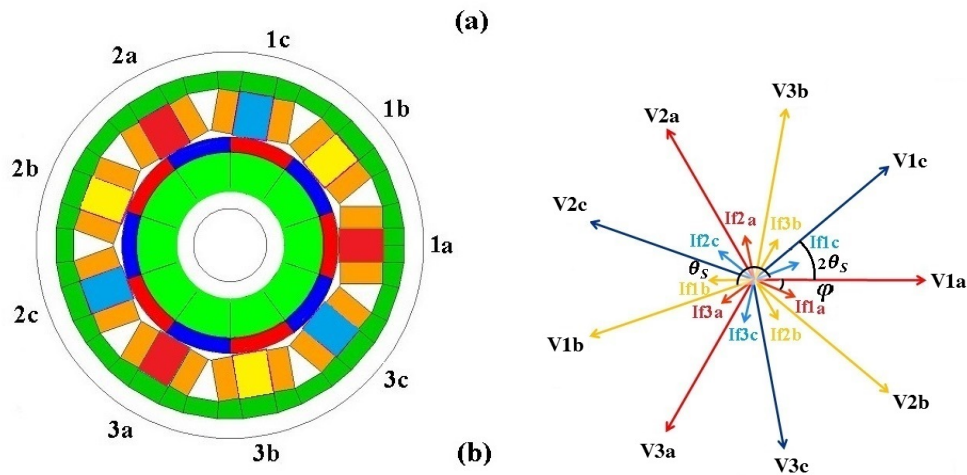


Figure 3. Case III: base machine (a) and phasor diagrams (b). Red teeth and red star supplied by the converter a, yellow teeth and yellow star supplied by the converter b; blue teeth and yellow star supplied by the converter c. φ is the V-I phase angle and θ_s is the electric displacement between two sequential stars.

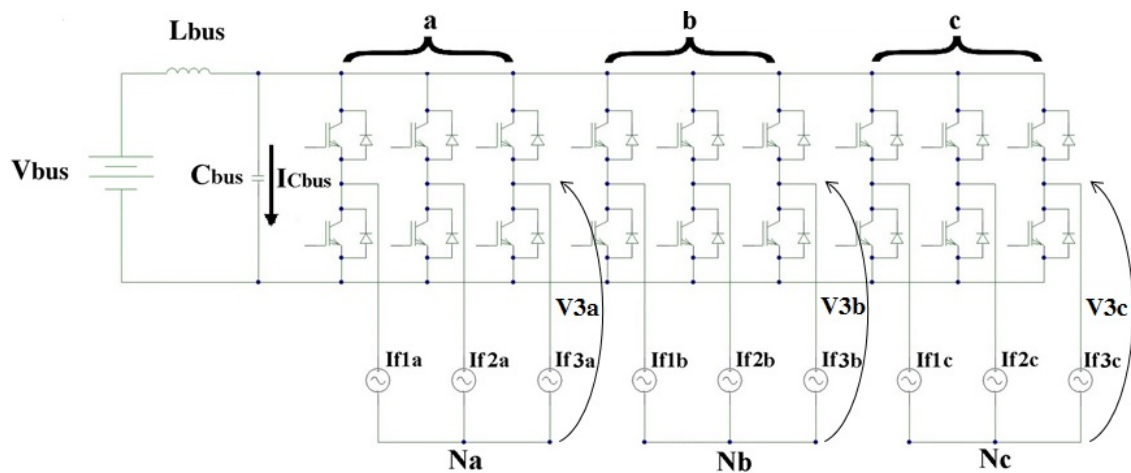


Figure 4. Triple-3-phase converter IGBT (Insulated Gate Bipolar Transistor) based. The converter is obtained from the composition of three 3-phase converter modules. Each 3-phase converter (a,b,c) supplies a 3-phase star of the machine.

In order to reduce the mutual coupling between phases belonging to difference stars, a double layer tooth coil open slot stator solution has been chosen. For the specific motor design, finite element analyses have been conducted. The value of the mutual inductance is equal to 1.37 mH, which is about 12.5% of the self inductance, equal to 10.9 mH. Instead, in a closed slot solution, the value of the mutual inductance is equal to 97% of the self inductance. In [21], the authors demonstrate that such a simple stator structure also allows, anyway, to obtain a very low torque ripple solution.

2.2. The Multi-N-Phase Converter

In case I, a classical 3-phase converter has been chosen. In II and III, a 9-phase converter (one leg for each coil of the base machine) like the one in Figure 4 has been adopted. The whole structure, in this latter case, has been designed as combination of modular parts. Each module is a 3-phase converter constituted by a control board and a power board based on a standard control and modulation technique.

2.3. Differences among Solutions

The solutions proposed in Figures 1a, 2a and 3a present a concentrated winding stator with the same number of poles. The concentrated winding stator introduces performance benefit, like simple construction, reduction of frontal connections and the related Joule losses reduction, and the constant power speed range can be properly increased thanks to the high Magneto Motive Force (MMF) harmonic contribution that affects the stator inductance [22].

The same stator structure produces different values of windings factor in the 3-phase solution (I, II) and in the triple-3-phase one (III). In particular, the solution III in Figure 3 has a greater fundamental winding factor [23,24].

From the slot winding layout in the electrical space, the windings factor for each phase has to be calculated as the ratio between the resulting electromotive force and the number of windings contribution for each phase [9,25,26].

The calculation of the winding factor for the 3-phase solutions (I, II) leads to the well known result of 0.945 for the $q_{3/10}$ machine [24] considering the synchronous harmonic, while the triple-3-phase solution (III) presents a winding factor equal to 0.985 for each star and then for the whole motor [15,27].

The ratio between the winding factor is equal to 0.96, which can be seen as an increase in the torque density of the triple-3-phase solution by 4%, or like an 8% reduction in the copper losses at the same delivered torque. Compared to cases I and II, case III then benefits in the copper losses reduction if the same DC-link voltage is chosen.

The per unit phase ripple current is reduced by a factor near to 3 for cases II and III machines compared to I. As a first approximation, in fact, the same phase voltage means to have three times the wires in each coil with an inductance that grows by a factor of 9. Phase current is reduced by a factor 3, and their ratio presents an advantage of a factor 3 in the per unit phase current ripple (at the same modulation frequency and the same motor voltage). In the end, it is possible to notice that the solution III with respect to the solution I, II allows for obtaining a reduction of the harmonic content of the MMF with benefits in terms of torque ripple [6,21]. Table 1 summarizes, for each case, the values of winding factor (K_w), the harmonic order h of the first torque ripple harmonic (T_h) and the per unit phase ripple current (i_{rpu}), normalized to the corresponding values of the case I.

Table 1. Differences among solutions.

Solution	K_w/K_{wI}	T_h/T_{hI}	i_{rpu}/i_{rpuI}
I	1	1	1
II	1	1	1/3
III	1.04	2	1/3

In the next section, only solutions I, III will be considered.

3. PWM Control Strategy

What here is defined as PWM control strategy considers using only modular inverters to supply each 3-phase stars in the proposed triple-3-phase motor. The modulation technique used is an extension of the Sine triangle PWM (SPWM) with the common mode injection calculated from the balanced envelopment [28] where different carriers have been used for each inverter. The aim of the proposed paper is to understand what is the best phase shifting between carriers in order to reduce the DC-link rms current in any operating condition for a triple-3-phase drive. The modular structure of the converter offers different choices in how to supply the different machine portions. Then, for the triple-3-phase solution, the 3 coils of the first star are supplied by the first inverter, the second star by another and so on (Figure 4).

The 3 converters have to generate 3 voltage stars as in Figure 3b. The electrical displacement between two sequential stars is defined by θ_S electrical degrees [8]:

$$\theta_S = \frac{N_P \cdot \pi}{N_C \cdot N_S} \quad (1)$$

where:

N_C : Number of Coils that constitute star (considering always one coil per phase, here $N_C = 3$),

N_S : Number of Stars (here $N_S = 3$),

N_P : Number of Poles (i.e., PM) (here $N_P = 10$)

For a 3-3-10-1 machine, $\theta_S = 200$ electrical degree.

The drive control block scheme is reported in Figure 5. Three identical rotor synchronous Direct and Quadrature (dq) current references are generated from a lookup table (LUT), one for each star. For the proposed motor structure, the mutual coupling between phases being negligible, the dq reference currents are equal to each other for each control loop in healthy and fault conditions. In fact, no variation in the control strategies have been considered under fault.

In the proposed control scheme, three different synchronous carriers' signals have to be used in the PWM control of the three sections. Triangles carried with the rising and falling symmetrical edge have been used in this work.

Different relationships between the carriers signals can be chosen. In this paper, the effects of a specific simple relationship between PWM carriers are discussed: the three carriers have the same mutual phase shift, as shown in Figure 6. In Figure 7, an example of the relationship between carrier has been reported for an angle $\alpha_u = 45$ degrees.

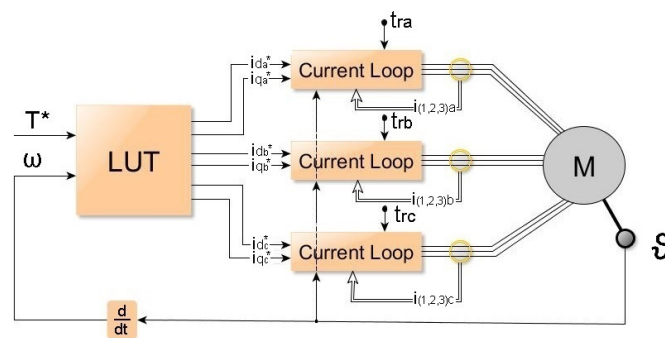


Figure 5. Control block scheme for the triple-3-phase drive.

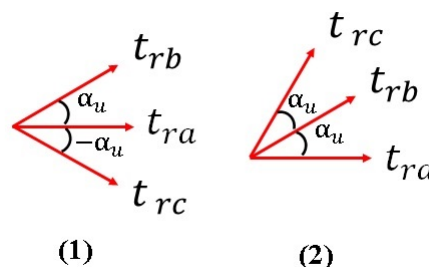


Figure 6. Phase shifting between carriers' signals: the results of the analysis are the same regardless of the reference (1,2) if the three PWM carriers' signals have equal mutual phase shift.

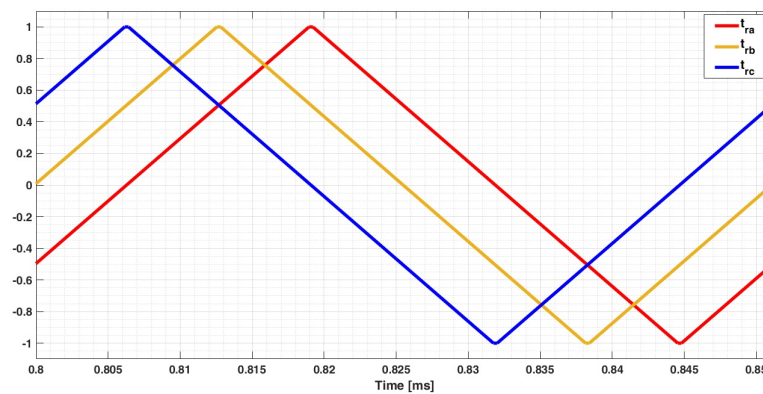


Figure 7. Carriers t_{ra}, t_{rb}, t_{rc} related to an angle of $\alpha_u = 45$ degrees.

It is worth noticing that the introduction of a displacement between PWM carrier could also positively affect output torque ripple [29]. In this analysis, the authors focused on the effect of the control technique on the converter side.

In the following section, a detailed analysis based on simulations of the effect on the DC-link capacitor bench of the described PWM strategy is proposed.

The results will be compared with the one of the classical 3-phase drives. The reference case is then the case I, i.e., the 3-phase machine supplied by a single inverter with a simple current control in SPWM.

4. Reduction of the DC-Link Rms Current

The DC-link capacitor is one of the key converter components that affects the cost and the dimension of the whole drive [30]. Furthermore, physical shapes of the capacitors themselves could lead to difficult practical solutions, resulting in a large commutation path for the inverter switches with the related over-voltage problems.

DC-link operating voltages, $I_{C_{rms}}$ and working temperature strongly influence the DC link capacitor life, and then the whole drive life [31].

DC-link capacitor performs several functions in the converter [31]: to allow for compatible hard switching commutations firstly, to allow for the impulsive inverter currents supply, to absorb the energy of the drive in the case of converter fault and to assure energy for power peaks. The DC-link current, immediately out of the switching legs, is composed of a DC value and all harmonics related to the switch commutations. These last represent, in ideality, the DC-link capacitor current $I_{C_{rms}}$. The relationship between the switching commutation and the DC link current waveform is reported in [32]. In general, $I_{C_{rms}}$ decreases as the number of phases increases [32,33], and the value of the $I_{C_{rms}}$ changes for different operating conditions.

As an example, for a three-phase machine supplied by a single inverter, the simulated $I_{C_{rms}}$ values for different operating condition are reported in Figure 8 [31].

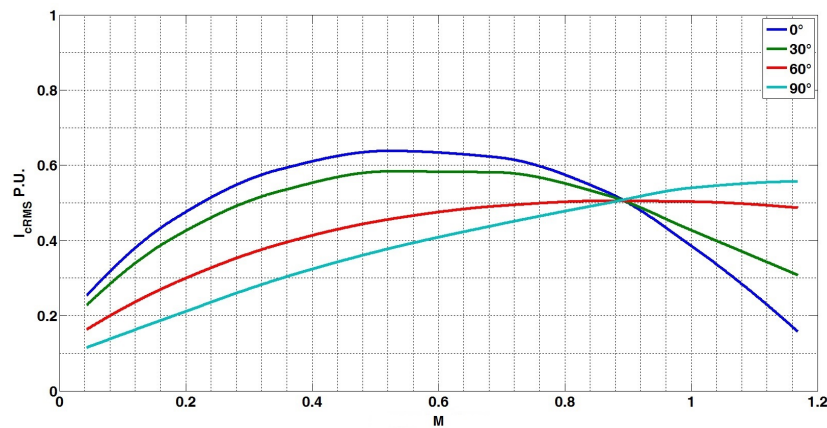


Figure 8. Simulated results: 3-phase machine $q_{3/10}$ 3-phase powered: $I_{C_{rms}}$ variation vs. M for different value of electric angle between voltage and current (VI-phase).

In order to have a complete description, simulations have been carried out for the 3-phase drive and for the triple-3-phase one.

The software used in simulation is Pspice ORCAD (16.5, CADENCE, San Jose, CA, USA). The switching frequency is fixed to 20 kHz and dead time is not considered.

In the 3-phase solution, a single inverter supplies a motor simply modelled by current generators. No ripple current effect is considered at the simulation level.

In the triple-3-phase solution, each module of the triple-3-phase converter (Figure 4) supplies one 3-phase star as in Figures 2 and 3. The motor is again modelled by nine current generators with a current value equal to 1/3 the 3-phase one.

When disregarding the output current ripple in a current loop control, the motor can be simply modelled by current generators, at least from the DC-link point of view. This approximation can in general be done as shown in [31,34]. In addition for fractional slot concentrated windings machines, like that one considered in the paper, to assume a negligible ripple current with respect to the nominal phase current became more consistent since the phase inductance is typically high.

The variations of the following parameters have been considered:

- Modulation index ($M = 2 V_{Phase}/V_{bus}$),
- Displacement between PWM carriers (α_{it}),
- Phase between voltage and current (VI-phase).

Steady state simulations have been performed in all of the possible combinations of the above parameters. In particular, to consider different values of VI-phase means evaluating the effect of the proposed technique through different operating conditions like the one related to the constant power speed range in which the rotation of the current vector leads to a variation of the machine power factor. The $I_{C_{rms}}$ values and their harmonic spectrum for all the operating points have been stored and proposed here.

All results have been normalized to ease the comparison. DC-link rms currents have been normalized to the rms phase current value of the 3-phase case. The modulation index M can vary from 0 to $2/\sqrt{3}$ according to the linear inverter range extended through the injection of the proper common mode [28]. Data processing has been performed to search for the worst case in all conditions in order to guide for the DC-link capacitor choice. In the following, the more representative results are proposed.

4.1. Three Phase Solution

For the 3-phase solution, the values of the $I_{C_{rms}}$ for different operating conditions are collected in Figure 8. The worst case is the one presenting the maximum $I_{C_{rms}}$ value. The capacitor design value is $I_{C_{rms}} = 0.64$ p.u.

4.2. Triple-3-Phase Solution

In the triple-3-phase case, $I_{C_{rms}}$ also depends on M and VI-phase. In Figure 9, it is possible to see the benefits of the introduction of the displacement α_u . The graph in Figure 9 refers to the M value for which the greatest variation with α_u in the $I_{C_{rms}}$ values occurs. It can be noticed that, for the chosen M value, the $I_{C_{rms}}$ is barely constant independently of the VI-phase for four values of α_u : 50, 70, 110, 130 carrier degrees approximately.

For different operating conditions, the minimum of $I_{C_{rms}}$ moves and the trend becomes barely flat as the M increases, as it will be shown in detail in the experimental part.

In Figure 10, a 3D picture collects all results for all simulated M , α_u and VI-phase values.

Once the drive operating condition is known, it is possible to define the DC-link design point. To make the analysis independent from any particular application, it is possible to assume that the drive could explore all the operating conditions (all VI-phase/ M couples). It is possible to see how, for the triple-3-phase machine, it is sufficient to calculate the value of $I_{C_{rms}}$ for VI-phase 0 and 90 in order to calculate the worst case design. In Figures 11 and 12, iso-level curves report the value of the $I_{C_{rms}}$ in p.u with respect to the rms value of the phase current. It is possible to see how the design point is influenced by the operating conditions. If the VI-phase variation is limited from 0 to 30 electrical degrees, two equivalent α_u angles are 60 and 120 degrees, but if the operating range of the drive is not well defined or the VI-phase moves from 0 to 90 electrical degrees, the design point moves and an optimal angle can be chosen around 45 degrees. Data in Figure 10 are then analysed in order to identify the locus of greatest values of $I_{C_{rms}}$ versus α_u in all operating conditions. Figure 13, in detail, represents this locus for each α_u carrier displacement. The mutual phase shifting between PWM carriers α_u for which the minimum of the greatest $I_{C_{rms}}$ occurs can be chosen as the optimal PWM displacement angle. In [15], the analysis on the simulated results includes the evaluation of the DC-link current harmonic contributions for different α_u . The design point chosen ($\alpha_u = 45$ degrees) is the point where the first and the second harmonic contributions meet each other.

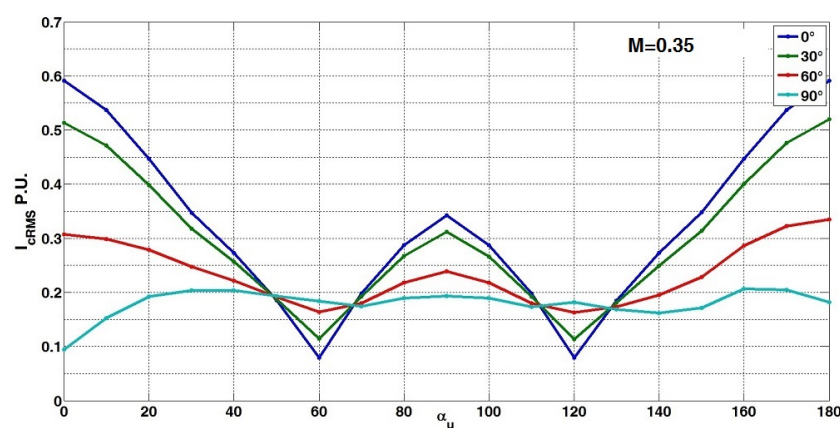


Figure 9. Simulated results triple-3-phase machine: $I_{C_{rms}}$ for different displacement between PWM carriers signals (α_u) and different V-I phases. At $M = 0.35$, there is the larger variation of $I_{C_{rms}}$ as function of α_u .

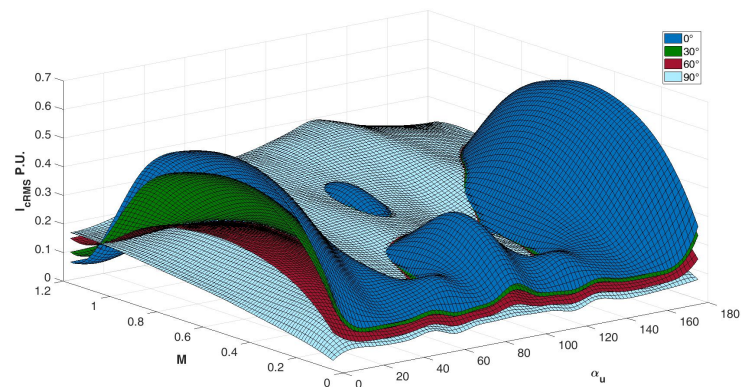


Figure 10. Simulated results: triple-3-phase machine: values of I_{cRMS} with the variation of M and α_u . Each surface represents a different value of VI-phase.

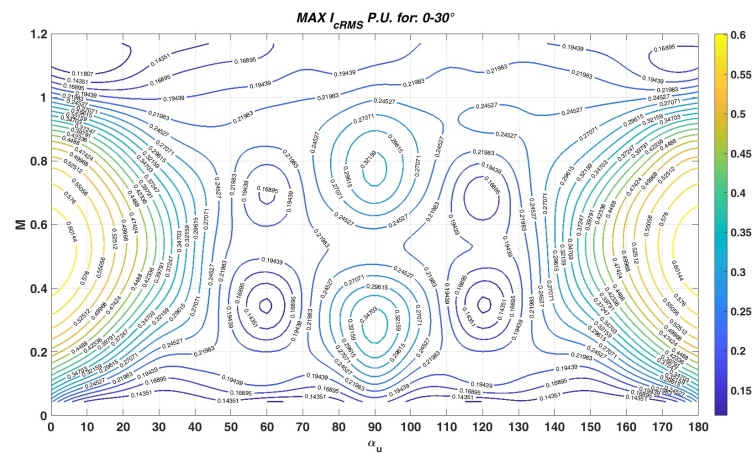


Figure 11. Simulated results: isolevel of greatest I_{cRMS} in p.u with respect to the rms value of the phase current worst case vs. α_u for all M value. The different figure represents the locus of the greatest I_{cRMS} values for a specific operating range.

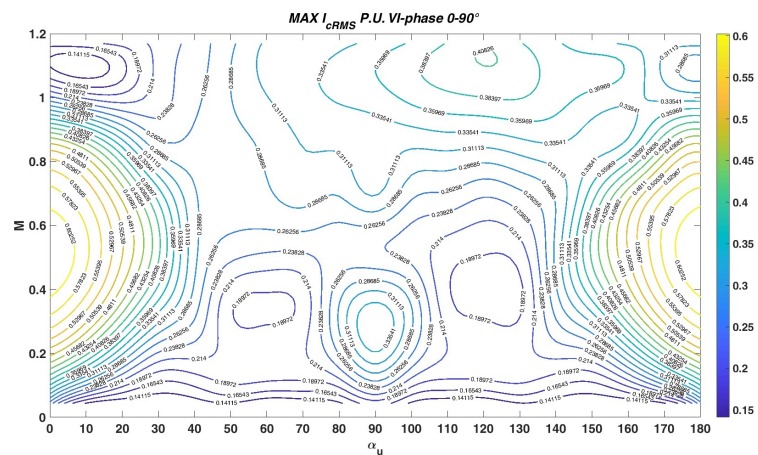


Figure 12. Simulated results: Isolevel of greatest I_{cRMS} in p.u respect to the rms value of the phase current worst case vs. α_u for all M value. The different figure represent the locus of the greatest I_{cRMS} values for a specific operating range.

The same analysis, described above, has been extended for α_u larger than 180 degrees and the collected results suggest a mirror behaviour. In addition, for V-I phases greater than 90 degrees, electrical degrees' graphs regress from the previously overlapping curves. As an example, the $I_{C_{rms}}$ resulting for 100 electrical degrees is equal to the one for 80 el. deg., and, for 180 el. deg., the curve overlaps the 0 one.

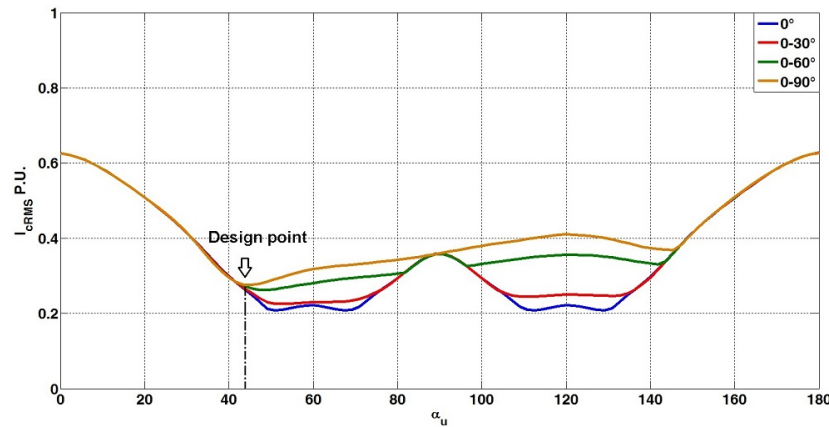


Figure 13. Simulated results: triple-3-phase phase machine: $I_{C_{rms}}$ worst case vs. α_u for all M value. The different curves represent the maximum $I_{C_{rms}}$ values for different V-I phases.

It is worth noticing that the simulation methodology proposed does not take into account any parasitism. In this ideal scenario, the proposed technique leads to a reduction close to 50% of the $I_{C_{rms}}$ p.u. The next section will show how the effectiveness of the proposed technique is affected by the converter layout.

5. Modulation in Fault Condition

As previously said, multi-n-phase drives offer additional degrees of freedom that can be used for different purposes, such as torque enhancement and design for fault tolerant strategies. The aim of this chapter is to highlight that, in the proposed PWM strategy, adjustments are not needed under fault operation. The displacement between carriers' signals are also applicable under single or double inverter faults appearing as a real additional DoF. The analysis is conducted considering only inverter faults. If one phase of one converter fails, the whole converter is removed. Considering the adopted fault scheme, then, the maximum number of admitted inverter faults is 2 (removing 6 inverter legs).

The analysis in the previous section is here repeated considering one modular converter fault at a time. The proposed configuration, in fact, is not symmetric and two different faults could occur. For a single inverter fault, then, the value of $I_{C_{rms}}$ depends on which converter has been shut down.

The $I_{C_{rms}}$ values for a double inverter fault are equal to the values for a 3-phase case (Figure 8) reduced by a factor of 3, i.e., a constant maximum value of 0.21 p.u independently of α_u .

Figure 14 shows the locus of the greatest $I_{C_{rms}}$ as a function of α_u for any value of V-I phase and M including fault conditions. The fault of the inverter **a** and **c** leads to the same points and to the same harmonic spectrum, highlighting the fact that the harmonic compensation depends on the mutual phase shift α_u independently from the sign of the shifting. The fault in the **b** inverter leads to the doubling of the adopted phase shifting.

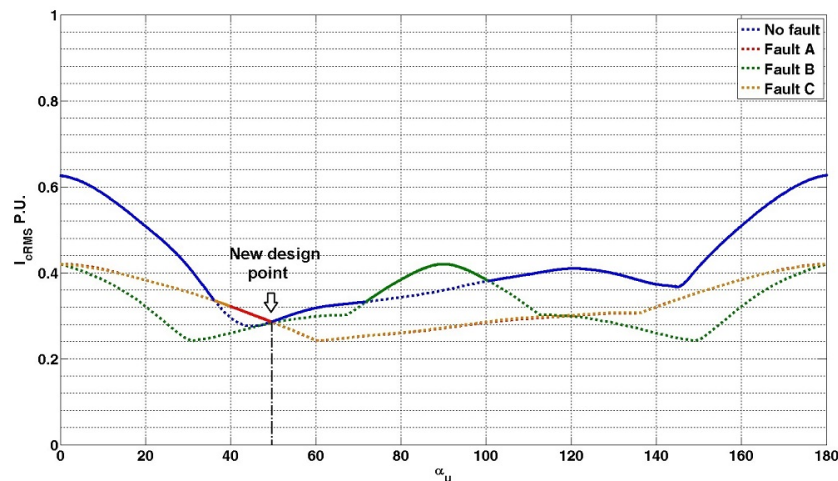


Figure 14. Simulated results: triple-3-phase machine: $I_{C_{rms}}$ worst case vs. α_u for all M/VI-phase combinations including in fault conditions.

Overlapping the locus of the greatest $I_{C_{rms}}$ for each fault condition, it is possible to define a new design point that represents the global best worst case design point for the proposed drive, considering all operating conditions (continuous line in Figure 14). The value of optimal displacement angle α_u on which the minimum $I_{C_{rms}}$ occurs, also including the fault conditions, is close to the design point that could be defined if no faults would have been introduced.

6. Experimental Results

Measurements from two different laboratory setups verify the simulated values of the $I_{C_{rms}}$ shown in previous sections. These verifications allow the simulated approach the capability to predict results and to define the appropriate design point. The reduction of the rms current in the capacitor has been tested for three asynchronous motors over a wide variety of operating conditions and then for a triple 3-phase motor. The tests are both essential in order to validate the effectiveness of the simulation methodology.

6.1. Three 3-Phase Motors Experimental Setup

The experimental setup adopted in this set of measurements is shown in Figure 15. In the following a detailed explanation of the various elements that composed the test bed is given.

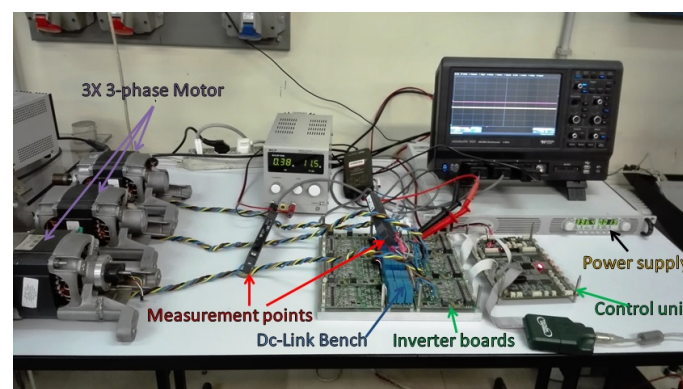


Figure 15. Experimental setup: three 3-phase motors, the control board and the power section with two triple-3-phase converter.

6.1.1. Power Electronics

The power electronics used for the test are composed of three inverter boards and a single control unit (Figure 15). Each inverter board has a capacitor (260 nF, 600 V) on each leg in order to reduce the commutation path as shown in Figure 16 and then to reduce the overvoltage related to the current commutation of each half bridge. The dimension of the ceramic capacitors are one order of magnitude lower with respect to the DC-link one (27 μ F); as a consequence, it is possible to consider that the biggest amount of the current ripple flows through the DC-link capacitor. The three boards are connected in a parallel way to a common DC-bus. If the internal DC-bus is not directly accessible, then the values of the $I_{C_{rms}}$ are estimated through the measurement of the AC current downstream the bus (inverters input). The measurement procedure itself affects the results. In fact, the wires introduced to allow for a high bandwidth current probe placing modify the characteristic impedance of the commutation path. The wires and the current probe introduce a parasitic inductance that reduces the amount of current that flows through the DC-link capacitor. In order to complete the analysis, the simulations have been extended including also parasitic parameters, commutation path capacitors and the parameters of the wires introduced for the measurement in Figure 17. Taking into account the parasitic parameters in the model is important since they affect the current flowing in the DC-link capacitor showing how the power electronics layout can affect the results. The values reported in Figure 17 have been estimated and are considered equal to $L_{par} = 1$ nH, $R_m = 5$ m Ω and inductance $L_m = 10$ nH.

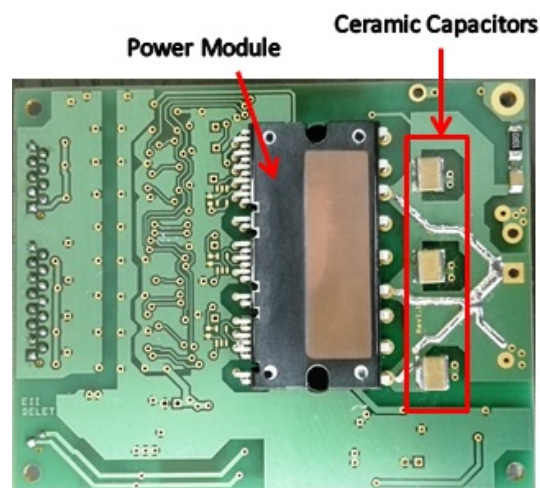


Figure 16. 3-phase module: in the picture, the Intelligent Power Module (IPM) and the ceramic capacitors introduced in order to reduce the commutation path are highlighted.

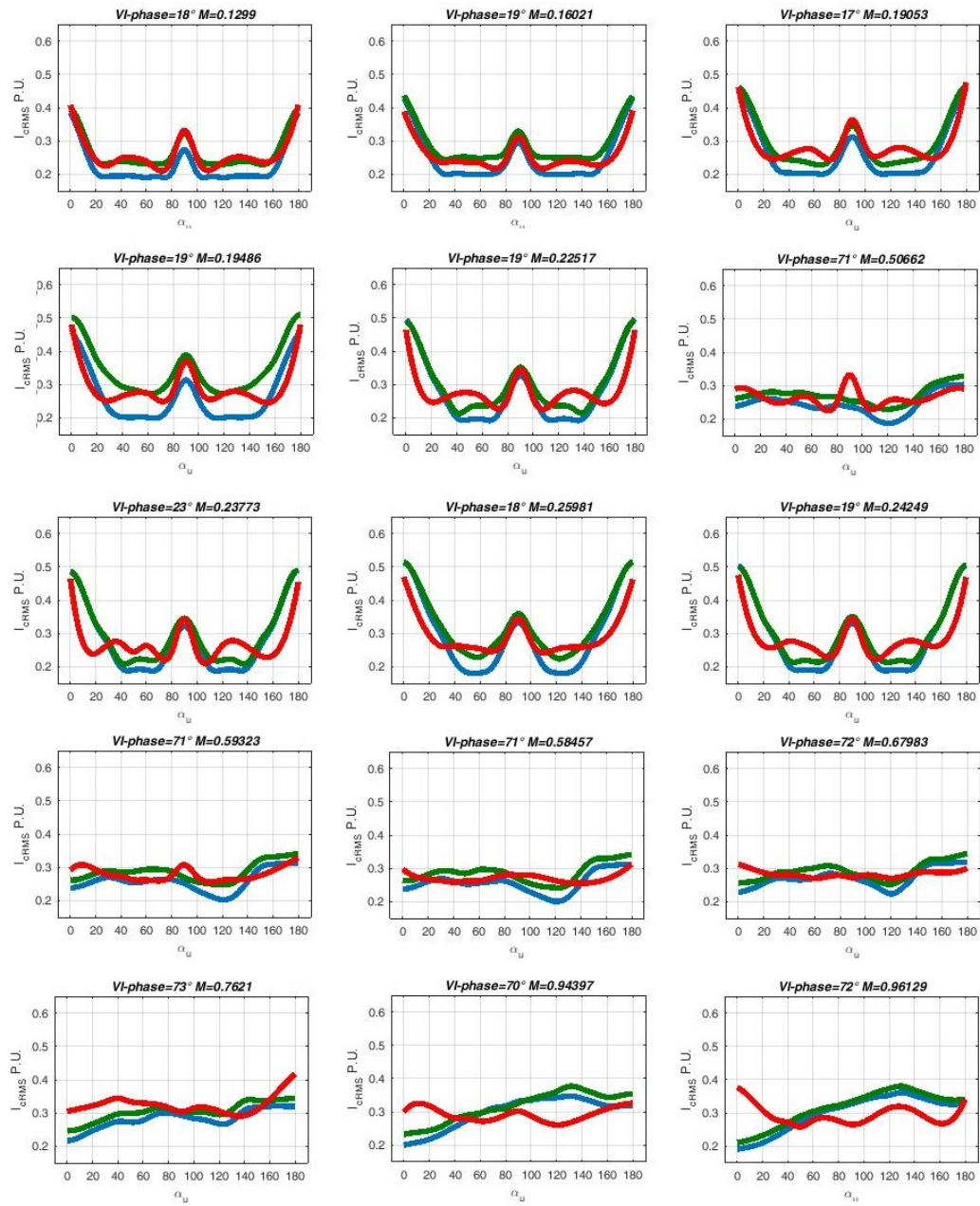


Figure 18. Each picture shows $I_{C_{rms}}$ values for different α_u and different couples of M and VI-phase. The blue line is the simulated $I_{C_{rms}}$ for triple-3-phase converter in Figure 4, the green line is the simulated $I_{C_{rms}}$ considering a model of the triple-3-phase converter used in the laboratory setup with estimated concentrated resonance parameters, and the red line is the measured evolution of $I_{C_{rms}}$ with the laboratory setup.

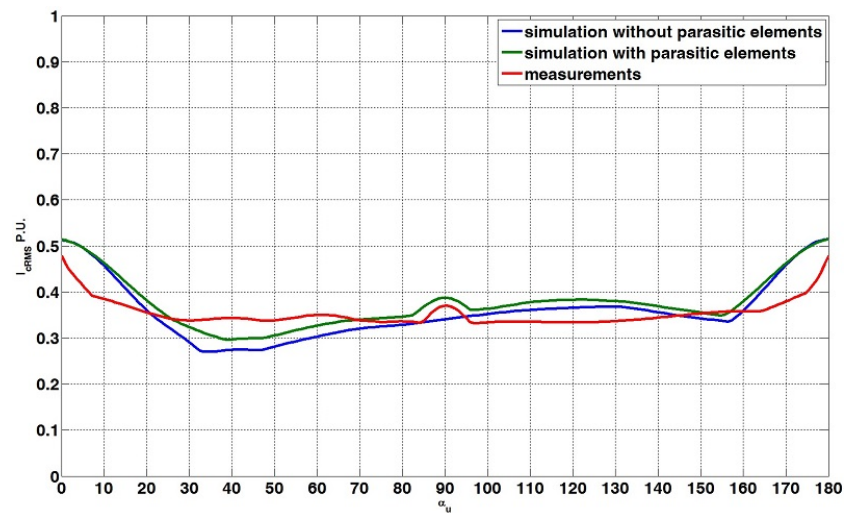


Figure 19. $I_{C_{rms}}$ worst case for α_u variation for considered couples of M and V -I phases values. The blue line is the simulated $I_{C_{rms}}$ for triple-3-phase converter in Figure 4, the green line is the simulated $I_{C_{rms}}$ considering a model of the triple-3-phase converter used in the laboratory setup with estimated concentrated resonance parameters, and the red line is the measured evolution of $I_{C_{rms}}$ with the laboratory setup.

Figures 18 and 19 show that the simulation model well represents the evolution of the phenomena with small differences for high M values. The comparison between the blue curve and the green one highlight that the $I_{C_{rms}}$ values depend by the converter layout. The proposed PWM control technique become more effective if a proper commutation path is adopted, as always happens.

The experimental results provide $I_{C_{rms}}$ trends for α_u close to what was expected. The difference between the green curve and red ones could be due to two factors:

- It is difficult to identify the resonant parameters of the commutation path. These parameters are estimated and inserts it in the simulation model as concentrated parameters.
- The DC bus is not directly accessible: as a consequence, the measurements are conditioned by the measurement procedure itself.

Simulation and experimental results, anyway, indicate that the α_u minimizing the $I_{C_{rms}}$ for the proposed PWM strategy is not a constant, and it is not necessarily equal to 120 carrier degrees as could be at first thought. The optimal α_u depends on the converter layout and by operating conditions. Downstream from the simulations and the experimental results, the present paper suggests defining the DC-link capacitor bench dimension considering a phase shifting between PWM carriers equal to 45 carrier degrees. In this case, it is possible to contain the value of $I_{C_{rms}}$ in all operating conditions, also considering faults operation. The adoption of this α_u value is advantageous also in terms of implementation being $1/8$ of the PWM period time, typically an integer number in the digital implementations.

6.2. Triple 3-Phase Motor Experimental Setup

In Figure 20, the test bench is reported. A triple 3-phase machine is driven by a I/Hz control with three equal dq reference current.

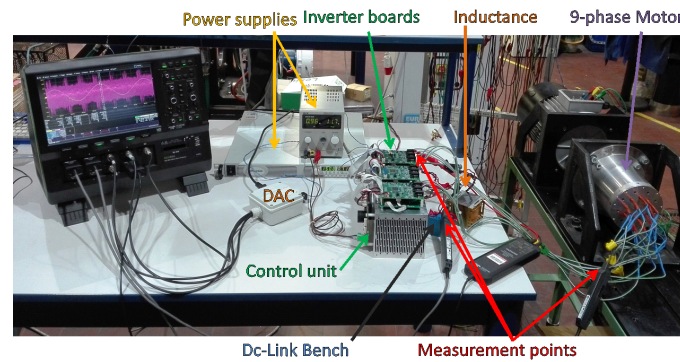


Figure 20. Experimental setup: triple 3-phase motors, the control board and the power section with one triple-3-phase converter.

6.2.1. Power Electronics

The power electronics used for the test is composed by three inverter boards and a single control unit (Figure 15). Each inverter board has a capacitor (1.1 μF , 600 V) on each leg in order to reduce the commutation path. An external DC-link film capacitor has been used (60 μF) and the DC-link capacitor is directly accessible. With respect to the previous case, a bigger DC-link inductance is presented (measured $L_m = 220 \text{ nH}$). Like has been done for the experimental setup previously presented, the estimated parameters shown in Figure 17 have been inserted in the simulation. Thanks to the chance to measure directly the DC-link capacitor current, the simulation and the experimental results become much more congruent and the effect of the parasitic element is more evident compared to the previous case.

6.2.2. Electrical Machines

In order to validate the control technique, a triple 3-phase machine like that one schematically represented in Figure 3 has been adopted. The motor used has been designed for automotive application with the characteristics reported in Table 3. The triple 3-phase motor under test does not present sinusoidal EMF as a consequence, if a I/Hz control is driven at low speed, the shape of the modulation indexes is not perfectly sinusoidal. In Figure 21, the value of a phase current and corresponding modulation index acquired by a Digital to Analog Converter (DAC) have been reported.

Two different operatives' conditions have been tested.

Table 3. 9-phase motor data

Characteristic	Value
Poles	10
Nominal Current rms	16 A
Nominal Voltage rms	400 V
Speed Max	8.000 rpm

In Figure 20, the test bench and the measurement layout have been reported.

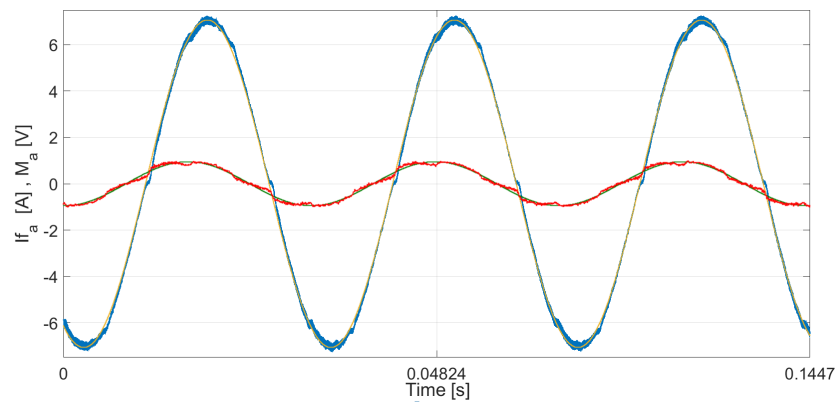


Figure 21. Measurement of the current (blue) and the corresponding modulation index (red) for a single phase. In yellow and green, the corresponding first harmonic of the waves is reported.

6.2.3. Experimental and Simulated Results Comparison

In Figure 22, results have been reported in detail for no load and on load conditions. For the same operatives' conditions, the simulations have been conducted without considering the parasitic parameters in order to compare the results with the ideal case. Measurements and simulations highlight how the parasitic parameters can affect the final results.

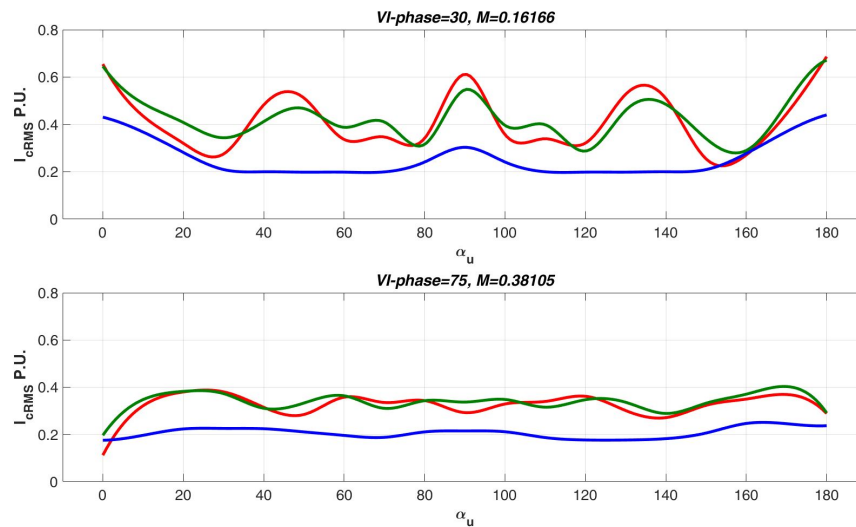


Figure 22. Each picture shows $I_{C_{rms}}$ values for different α_u and different couples of M and VI-phase. The blue line is the simulated $I_{C_{rms}}$ for triple-3-phase converter in Figure 4, the green line is the simulated $I_{C_{rms}}$ considering a model of the triple-3-phase converter used in the laboratory setup with estimated concentrated resonance parameters, and the red line is the measured evolution of $I_{C_{rms}}$ with the laboratory setup.

The main differences between the simulation and experimental results are related to:

- the error in the estimation of the resonance parameters of the commutation path,
- the realized modulation index is not purely sinusoidal due to the not perfect sine EMF at load operation

7. Conclusions

The present paper highlights some of the benefits of the multi-n-phase structures and presents a simple modulation strategy that reduces the DC-link stress in terms of rms capacitor current.

The approach to exploit an additional degree of freedom, the phase shifting between PWM carriers for the different 3-phase inverters, leads to a positive result and allows for the reduction of the DC-link rms current in multi-n-phase drives. The analysis of the benefits introduced by PWM phase shifting are extended to all possible operating conditions including the fault one. Simulated and experimental results show that the optimal PWM phase shifting is not a constant and the $I_{C_{rms}}$ design point is affected by converters' layout as well as operating conditions.

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