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Preface

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(Article begins on next page)

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The increasing integration and complexity of electronic system design requires a constant evolution of the languages used, and its associated design methods and tools. The *Forum on specification & Design Languages* (FDL) is a well-established international forum devoted to dissemination of research results, practical experiences and new ideas in the application of specification, design and verification languages to the design, modeling and verification of integrated circuits, complex hardware/software embedded systems, and mixed-technology systems.

FDL is the main platform to present and discuss new trends as well as recent works in this domain. FDL 2017 is the 20th in a series of events. Thanks to the commitment of the authors, presenters and panelists the 2017 edition of FDL was an extremely interesting and lively event.

This book is devoted to FDL 2017 and contains the papers that have been evaluated best by both the members of the program committee and the participants of the forum, which took place in September 2017 in Verona, Italy. It reflects thereby the wide range of topics that have been covered at this event. The selected contributions particularly highlight the increasing role of new modeling and verification methodologies targeting digital and analog systems, firmware development as well as functional and non-functional properties. By this, the portfolio of papers in this book provides an in-depth view on the current developments in our domain, which surely will have a significant impact in the future.

We would like to thank all authors for their contributions as well as the members of the organizing and program committees and the external reviewers for their hard work in evaluating the submissions. Special thanks go to Franco Fummi and his team from the University of Verona, who were responsible for a splendid organization of FDL 2017, and to Samarjit Chakraborty for his work as program co-chair. Finally, we would like to thank Springer for making this book possible.

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