## Investigating the properties of interfacial layers in planar Schottky contacts on hydrogen-terminated diamond through DC/small-signal characterization and radial line small-signal modelling

## Supplemental Material

## Mathematical derivation of the distributed radial line input admittance

We refer in the following to the equivalent circuit in Fig. 4 of the manuscript.

The per-unit-length parameters (p.u.l) of the radial line can be derived by considering a circular section of length dr with radius r, whose parallel capacitance and conductance model the gate/IL/channel cross section, whereas the series resistance describes hole transport across the channel:

$$dC = C_{\rm GC} 2\pi r dr$$

$$dG = G_{\rm d} 2\pi r dr$$

$$dR = R_{\rm ch} \frac{dr}{2\pi r}$$
(1)

where, with reference to Fig. 4, the gate-channel capacitance  $C_{\rm GC}(V)$  is defined as

$$C_{\rm GC}\left(V\right) = \frac{C_{\rm IL}C_{\rm ch}}{C_{\rm IL} + C_{\rm ch}}.$$
(2)

The parameters  $C_{\rm IL}$  and  $C_{\rm ch}(V)$  are the per-unit-area IL and channel capacitance, respectively. In Eq.(1),  $G_{\rm d}$  is the per-unit-area conductance accounting for the gate-channel leakage current. Finally,  $R_{\rm ch} = 1/(q\mu_h p_s)$  is the equivalent channel sheet resistance, where  $\mu_h$ and  $p_s$  are the 2DHG mobility and sheet density, respectively. The 2DHG concentration is evaluated self-consistently with the gate-channel capacitance of the circular section as

$$p_{\rm s}(V_{\rm G}) = -\frac{1}{q} \int_{V_{\rm G0}}^{V_{\rm G}} C_{\rm GC}(V) \,\mathrm{d}V \tag{3}$$

where  $V_{\rm G0}$  is a bias point in the off state, such that  $p_{\rm s}(V_{\rm G0}) = 0$ .

Applying the Kirchhoff's voltage and current laws to a line section of radial length dr located in r, the line equations result as

$$\frac{dV}{dr} = -\frac{R_{\rm ch}}{2\pi r} I$$

$$\frac{dI}{dr} = -Y_{\rm p} 2\pi r V$$
(4)

with  $Y_{\rm p} = G_{\rm d} + j\omega C_{\rm GC}$ . Differentiating, substituting and exploiting the variable change  $x = \sqrt{Y_{\rm p}R_{\rm ch}}r$ , we have for the line voltage

$$x^{2}\frac{\mathrm{d}^{2}V}{\mathrm{d}x^{2}} + x\frac{\mathrm{d}V}{\mathrm{d}x} - x^{2}V = 0$$
(5)

i.e. a zero-order Bessel differential equation, admitting a closed-form solution in terms of modified Bessel functions of the first (I) and second (K) kind. The resulting values for the line current and voltage are, respectively:

$$I(r) = A2\pi r \sqrt{\frac{Y_{\rm p}}{R_{\rm ch}}} I_1(kr) + B2\pi r \sqrt{\frac{Y_{\rm p}}{R_{\rm ch}}} K_1(kr)$$
$$V(r) = AI_0(kr) + BK_0(kr) , \qquad (6)$$

where A and B are integration constants, and  $k = \sqrt{Y_{\rm p}R_{\rm ch}}$ . The line current is derived from the voltage by differentiation. Finally, considering a line with radius R and applying the boundary conditions I(r=0) = 0 and  $V(r=R) = V_0$ , the line input admittance is calculated as

$$Y_{\rm i} = \frac{I(R)}{V(R)} = 2\pi R \sqrt{\frac{Y_{\rm p}}{R_{\rm ch}}} \frac{I_1(kR)}{I_0(kR)}.$$
(7)

The total equivalent impedance of the device is then computed by including the effect of the parasitic access and contact resistances  $(R_s)$  as  $1/Y_i + R_s$ .

The analytic model has been validated against 3D physics-based simulations<sup>1</sup>, under AC condition, of a planar device with 5 nm thick IL ( $\epsilon_r = 9$ ), carrier mobility of 50 cm<sup>2</sup>V<sup>-1</sup>s<sup>-1</sup>, and radius of 25  $\mu$ m. Fig. S1 shows the parallel capacitance and conductance associated with the small-signal admittance  $Y_i = G_i + j\omega C_i$  as derived from the physics-based simulation and as predicted by Eq. (7), demonstrating the high accuracy of the analytic distributed model.

As discussed in the manuscript, reducing the gate radius to a few  $\mu$ m will make the dispersive effect of the channel resistance ( $R_{ch}$  in Fig. 4 of the manuscript) negligible, thus allowing the accurate extraction of the physical parameters by a lumped model that can be derived from the series expansion of Eq. (7) when  $kR \rightarrow 0$ . Truncating the series at the 4th order, one obtains

$$Y_{\rm i,lumped} = \pi R^2 Y_{\rm p} - \frac{\pi R^4}{8} Y_{\rm p}^2 R_{\rm ch}.$$
 (8)

The approximation admits for an interpetation in terms of a lumped equivalent circuit made by the series of the admittance  $\pi R^2 Y_{\rm p}$  and the resistance  $R_{\rm ch}/8/\pi$ , as can be demonstrated from the series expansion of the input impedance  $1/Y_{\rm i}$ . Thus, in the lumped limit,



FIG. S1.  $C_i/V$  (a) and  $G_i/V$  (b) curves at various frequencies of a large area planar device as predicted by physics-based simulations (symbols) and by the analytical distributed model in Eq. (7) (solid lines). Frequency points are logarithmically equally spaced between 10 kHz and 10 MHz.

the equivalent resistance of the line is the sheet resistance  $(R_{\rm ch})$  corrected by a suitable scale factor  $(1/8/\pi)$ .

## REFERENCES

<sup>1</sup> "Sentaurus Device, Synopsys Inc." (2014).