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## ABSTRACT

Over the past 30 years or more, chalcogenide phase-change materials and devices have generated much scientific and industrial interest, particularly as a platform for non-volatile optical and electronic storage devices. More recently, the combination of chalcogenide phase-change materials with photonic integrated circuits has begun to be enthusiastically explored, and among many proposals, the all-photonic phase-change memory brings the memristor-type device concept to the integrated photonic platform, opening up the route to new forms of unconventional (e.g., in-memory and neuromorphic) yet practicable optical computing. For any memory or computing device, fast switching speed and low switching energy are most attractive attributes, and approaches by which speed and energy efficiency can be improved are always desirable. For phase-change material-based devices, speed and energy consumption are both enhanced the smaller the volume of phase-change material that is required to be switched between its amorphous and crystalline phases. However, in conventional integrated photonic systems, the optical readout of nanometric-sized volumes of phase-change material is problematic. Plasmonics offers a way to bypass such limitations: plasmonic resonant structures are inherently capable of harnessing and focussing optical energy on sub-wavelength scales, far beyond the capabilities of conventional optical and photonic elements. In this work, we explore various approaches to combine the three building blocks of Si-photonics, resonant plasmonic structures, and phase-change materials to deliver plasmonically enhanced integrated phase-change photonic memory and computing devices and systems, underlining the inherent technical and theoretical challenges therein.

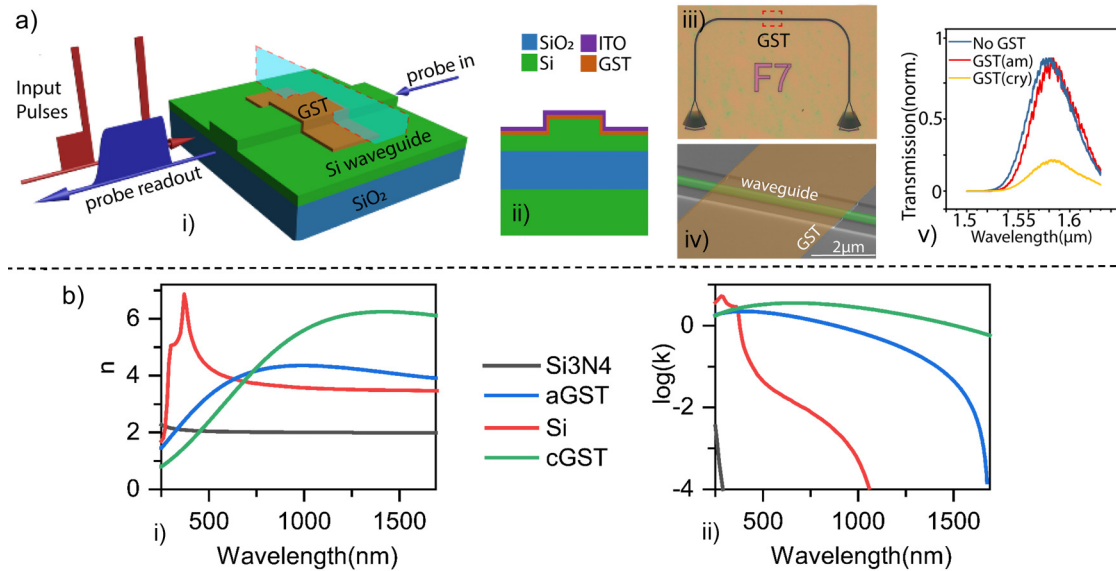
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## I. INTRODUCTION

Silicon photonics is now a relatively mature and established technology, and one that is at the very center of the scientific community's attention<sup>1-6</sup> One of the main reasons for this is the inherent energy efficiency and wider bandwidth of the optical signal transport channels, as compared to electrical interconnects. The marked advantages of photonic circuits for signal transport mean that silicon photonics is already used extensively in data centers, and for board-to-board and chip-to-chip communication, and has also seen preliminary experimental application for on-chip signal

transport.<sup>7</sup> More recently, the combination of Si-photonics and chalcogenide phase-change materials, or PCMs, (similar to those used, for example, in re-writeable DVD and Blu-ray disk formats) has led to the development of novel integrated optical memory and computing devices<sup>8-13</sup> Here, the low-loss, high-bandwidth, and innate parallelism (e.g., via the use of wavelength division multiplexing or WDM) of the photonic approach, in tandem with the multi-state programming capabilities of chalcogenide PCMs, portend a new generation of fast, low-power computer processors (or co-processors) that exploit in-memory and neuromorphic computing approaches.<sup>14-20</sup>

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**FIG. 1.** Photonic phase-change memory device schematics and operating principles. Reproduced with permission from Li *et al.*, *Optica* 7, 218 (2020). Copyright 2020 Author(s), licensed under a Creative Commons Attribution (CC BY) License. (a) Device geometry and experimental data. (i) Device architecture and schematic of the operating principle. (ii) Device cross section as represented in (i). (iii) Optical image of a test device. (iv) SEM image of the device represented in (iii). (v) Transmission data for the device in (iii) as a function of the wavelength. (b) Refractive index (i) and extinction coefficient (ii) of the device materials.

The basic concept of the integrated phase-change optical memory device is shown in Fig. 1; a thin chalcogenide PCM layer is fabricated on the top surface of a conventional photonic integrated waveguide [see Fig. 1(a)]. Specifically, the PCM here adopted is the archetypal chalcogenide phase-change alloy Ge<sub>2</sub>Sb<sub>2</sub>Te<sub>5</sub> (or GST for short). Optical pulses sent down the waveguide [the “input” pulses in Fig. 1(a)] evanescently couple to the PCM layer and enable it to be switched between its crystalline and amorphous states, or to one of many intermediate levels of crystallinity lying between these states. Since the refractive index of PCMs is highly phase-state dependent, as shown in Fig. 1(b), the programmed phase-state of the PCM layer in turn controls (or programs) the optical transmission of the waveguide, and it is this transmission modulation that is used in the readout process [indicated by the “probe” pulse in Fig. 1(a)] to determine the “information” stored in the PCM cell.

As of today, up to 35 levels (>5 bit) can be reliably read, written, and erased on a single PCM cell,<sup>11</sup> and a demonstrative 512 bit memory device has also been successfully fabricated and tested.<sup>12</sup> Additionally, multi-state PCM cells of the type shown in Fig. 1 have been used to provide arithmetic functionality,<sup>15,18</sup> have been incorporated into novel photonic crossbar arrays to deliver ultra-fast matrix-vector multipliers,<sup>17</sup> and have been used to realize synaptic and neuronal “mimics”<sup>10</sup> and even small-scale neuromorphic processors.<sup>14</sup>

The “conventional” phase-change photonic unit cell of Fig. 1, however, typically requires switching energies of a few hundred pJ, and switching times in the tens to hundreds of nanoseconds.<sup>8,9,11</sup> Approaches that can reduce switching energy requirements, and

increase switching speeds, will obviously be of much benefit. Since the PCM switching process is bound to unavoidable fundamental physical limitations, namely, the heating of the PCM unit cell, improvements in switching speed and energy are most likely to be achieved via two scenarios: a reduction of the unit-cell volume or a magnification of the light–matter interaction. Plasmonics can provide us with access to both these scenarios. Indeed, plasmonics offers additional light manipulation tools, otherwise inaccessible with conventional photonics. The collective oscillation of conduction electrons in a suitably shaped metallic nanoparticle (the so-called *localized surface plasmon*, LSP) can couple with impinging radiation, which in turn squeezes light into much reduced volumes, and greatly magnifies the local electric field, usually leading to a much reduced (non-diffraction limited) device footprint. In this work, we aim to underline the as yet untapped potential for the realization of fast, energy-efficient photonic memory and computing devices arising from the union of the energy-efficient silicon photonics platform, the sub-wavelength light-squeezing and field-enhancing capability of plasmonic resonant structures, and the intrinsic tuneability functionality brought by PCMs.

## II. A SHORT OVERVIEW OF PLASMONICS-ENHANCED PHOTONICS

The general light manipulation properties offered by metallic resonant nanoantennas have been widely reported (see, e.g., Refs. 21–28). The application potential of plasmonically enhanced conventional silicon photonics devices (i.e., those not incorporating PCMs) is also quite well explored, with plasmonically enhanced

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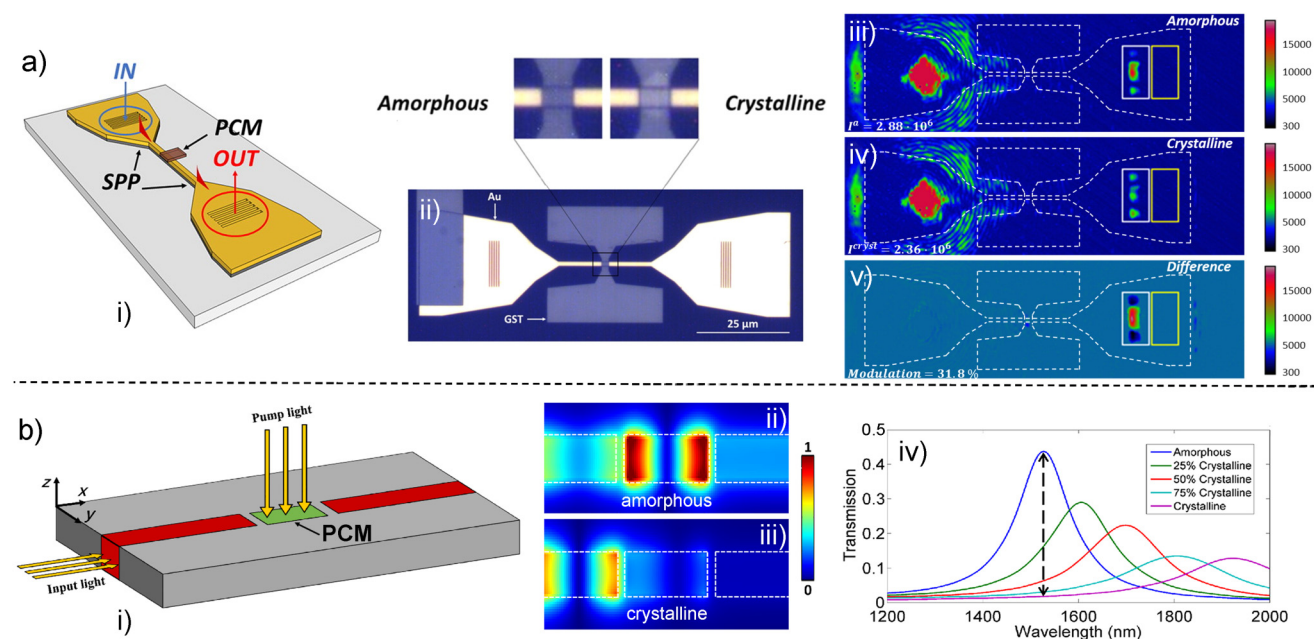
Si-photonics having already been demonstrated for sensing,<sup>29–32</sup> optical nanofocusing,<sup>33</sup> lasing,<sup>34</sup> emitters,<sup>35</sup> optical mode control<sup>36–38</sup> and coupling,<sup>39</sup> scattering control,<sup>40,41</sup> photodetectors,<sup>42</sup> and more. Also the use of hybrid plasmonic waveguides, either SPP (surface plasmon-polariton) or MIM (metal-insulator-metal) types, has been successfully demonstrated for applications such as Mach–Zehnder modulators,<sup>43–46</sup> detectors,<sup>47</sup> signal amplifiers,<sup>48</sup> modulators,<sup>49</sup> and switches.<sup>50</sup>

In many of the cases discussed above, the properties/performances of the various plasmonically enhanced waveguide devices were locked-in at the design stage, though some hybrid plasmonic waveguide concepts do offer tuneability<sup>43–45,47–50</sup> in their response. Tuning of the response of metallic nanostructures can be roughly classified into two general cases: methods directly modifying the metal dielectric function (e.g., carrier injection) and methods inducing a variation of the dielectric function of the surrounding environment (e.g., carrier injection and carrier generation, thermo-optic effects, liquid crystals, chemical adsorption, photochromic molecules, MEMS-NEMS, matter displacement, and the focus of this work, PCM approaches). The use of PCMs to engender tuneability in plasmonic devices is particularly attractive, since PCMs have the appealing characteristics of both non-volatility and a remarkably large variation of optical properties between phases (amorphous and crystalline states). For example, as seen in Fig. 1(b), the archetypal PCM alloy GST exhibits, in the near IR,

differences in the real and imaginary parts of the refractive index of around  $\Delta n_{max} > 2$  and  $\Delta \kappa_{max} > 1$ . Indeed, the combination of PCMs with plasmonic nanostructures to provide active, dynamically tuneable (or reconfigurable) optical metasurfaces for the control of free-space light propagation is now quite a well-explored topic (see, e.g., Ref. 51), but the exploitation of PCMs to tune waveguide-mounted plasmonic devices is, at present, rather under-explored. We therefore discuss such a topic in Secs. III–V.

### III. PHASE-CHANGE ENABLED TUNEABILITY OF ON-CHIP PLASMONIC DEVICES

An early proposal that saw the combination of phase-change materials with plasmonics in an integrated silicon photonics type platform is found in Rudé *et al.*<sup>49</sup> [see Fig. 2(a)]. The authors demonstrated the transmission tunability of a hybrid Au/SiO<sub>2</sub> plasmonic waveguide, by varying the crystal fraction of an 80 nm thick GST layer fabricated on the top of the waveguide's surface. Switching of the PCM layer was here attained through an off-plane laser pulse, although electrical switching was also suggested as a possibility by the authors. The device shows a superior optical contrast when compared to alternative techniques for SP waveguide attenuation tunability, and while the authors underline how the switching energy is rather high (a 6.9 nJ figure is reported), the PCM non-volatility (and thus, the passive retention of the



**FIG. 2.** GST-enabled SP propagation tuneable devices. (a) Au/PCM hybrid waveguide. Reproduced with permission from Rudé *et al.*, ACS Photonics 2, 669 (2015). Copyright 2015 American Chemical Society. (i) Device schematic. (ii) Optical microscope image of the fabricated device; insets highlight device with GST in its amorphous and crystal phases. (iii) and (iv) Intensity images of scattered and transmitted light at  $\lambda = 1550$  nm for GST in its amorphous and crystal phases. (v) Difference of the collected signals reported in (iii) and (iv). (b) Tuneable propagation on the MIM waveguide. Reproduced with permission from Zhang *et al.*, Nanophotonics 13, 046009 (2019). Copyright 2019 Society of Photo-Optical Instrumentation Engineers. (i) Device schematic. (ii) and (iii) Simulated electric field (normalized) in the amorphous and crystal phases, respectively. (iv) Calculated transmission as a function of the GST crystal fraction.

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waveguide attenuation) is indeed an alluring and energy-efficient characteristic, compared to volatile technologies.

Another, and quite recent, proposal for combining PCMs with plasmonically enhanced photonic integrated circuit (PIC) devices includes that reported by Zhang *et al.*<sup>52</sup> and shown in Fig. 2(b). This reprises Rudé's concept for modulating SPP propagation, although this time in a MIM waveguide, with a GST region contiguous to (or interjecting) the dielectric core of the waveguide. The authors use this idea to configure both integrated switches and modulators. The study is purely numerical, and it does not pursue practical switching solutions, although the authors also propose the use of an external laser source. Additionally, the MIM waveguide inherits the high insertion loss (IL) previously seen in Rudé *et al.* Yet, the concept shows how a drastic reduction of the modulator footprint can be achieved, while maintaining a high optical contrast (here of up to ~70%) by use of a GST inclusion measuring only  $50 \times 200 \text{ nm}^2$  in size. This result highlights the potential for very small volumes of PCM to yield excellent control of waveguide propagation, when combined with appropriate plasmonic structures.

The work highlighted in Fig. 2 is concerned with modulator solutions that tune and control the propagation of SPPs. In Singh *et al.*,<sup>53</sup> the combination of plasmonics and PCM technology with more conventional silicon photonic platforms is explored. In this (simulation-only) work, the authors propose a GST-enabled photonic modulator that enhances the absorption functionality by use of a series of optimized gold nano-rings, fabricated on the top of a dielectric ridge waveguide [see Fig. 3(a)]. Such a structure by itself selectively absorbs a narrow portion of the spectrum, whose bandwidth is determined by the nanorings' geometrical parameters (i.e., radius and thickness). The authors embed a nanoring array on a silicon dioxide layer, built on the top surface of a conventional ridge waveguide. On top of this structure, a PCM layer is deposited and capped with an electrical contact, with the PCM switching process being carried out electrically. The absorption induced by the PCM layer, as well as the wavelength selectivity, is determined by the PCM phase. Importantly, the work of Singh *et al.* suggests how the use of low footprint nanostructures can have similar, if not higher, modulation performances than encountered in SPP waveguides or in MIM devices but at a much reduced IL.

Turning to the use of the combination of PCMs and plasmonics for photonic integrated circuit (PIC) type memory and computing devices, to our knowledge, only two additional works are currently published. One, by Gemo *et al.*,<sup>54</sup> seeks to improve the performance of the all-photonic phase-change memory [of the type shown in Fig. 1(a)] by exploiting the plasmonic resonance of a dimer nanoantenna fabricated on the top surface of an integrated waveguide [see Fig. 3(b)]. Such a device greatly enhances the electric field magnitude in the gap between the two halves of the dimer antenna, and with a PCM layer deposited into the gap region, the device (i) maximizes the light-matter interaction, (ii) changes its resonant properties upon variation of the phase-state (crystal fraction) of the PCM inclusion, and (iii) by use of a much-reduced PCM volume, requires a fraction of the switching energy of the conventional device architecture. The numerical investigation highlights how such a device, designed in a way to allow for practicable fabrication using standard lithographic techniques, is capable of yielding an optical contrast of roughly 12.5% (maintaining an

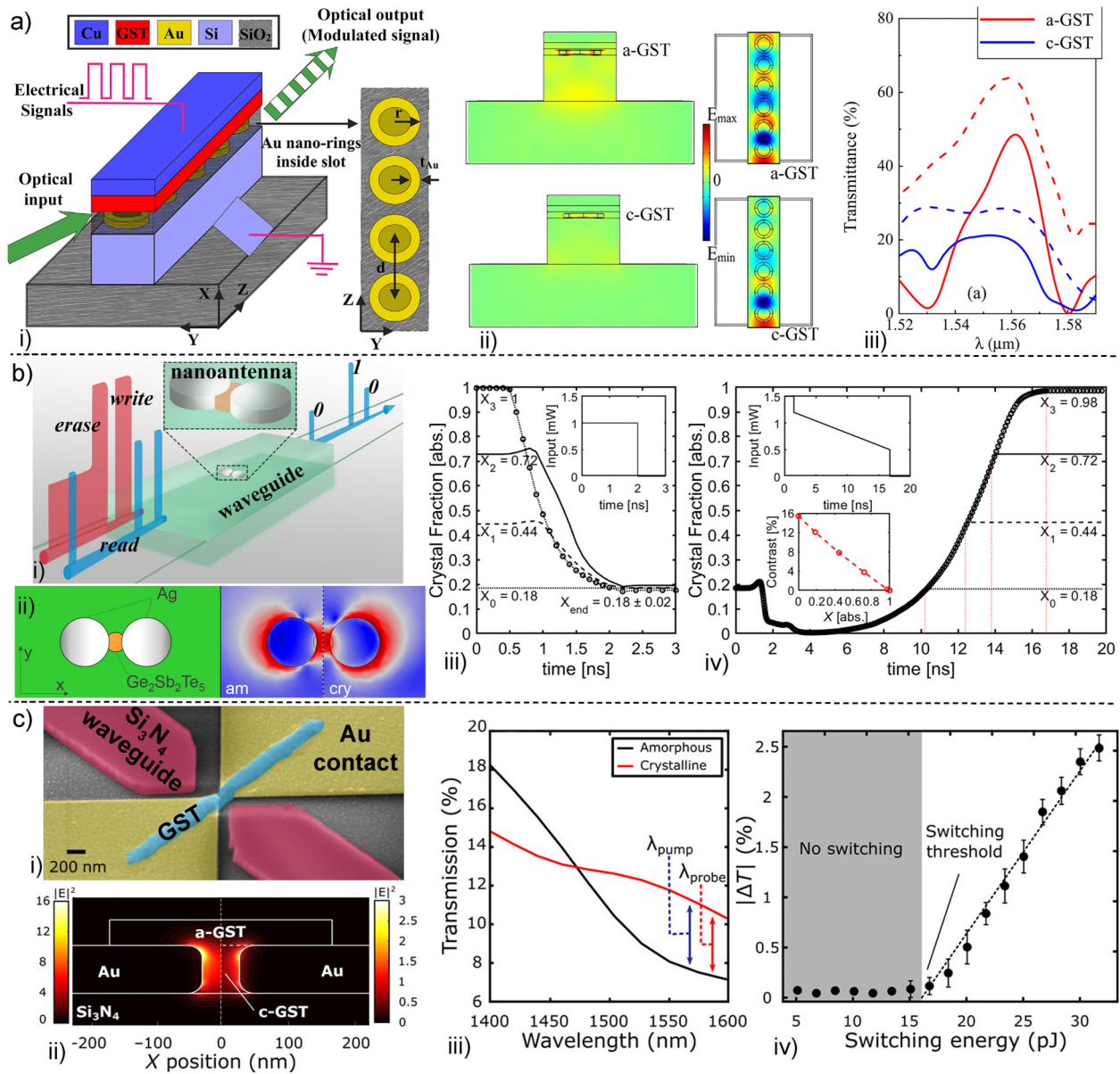
appreciably low IL of 0.38 dB), by use of a 2 pJ/2 ns energy write (amorphization) pulse and a 15 pJ/16.5 ns erase (re-crystallization) pulse—improvements of one to two orders of magnitude as compared to the conventional device. Moreover, by use of appropriately tailored write/erase pulses, four memory levels were arbitrarily addressed, demonstrating the multi-level storage functionality of the plasmonically enhanced PCM device. A later work<sup>55</sup> shows also the preliminary results of an experimental investigation of such devices, hinting to the confirmation of the expected behavior.

While in the work of Gemo *et al.*, switching of the PCM cell is achieved in the same manner as in the standard all-optical integrated phase-change memory device, i.e., by sending appropriate optical switching pulses down the waveguide, in the work of Farmakidis *et al.*,<sup>56</sup> a plasmonically enhanced approach with dual electrical/optical switching (and dual electrical/optical reading) is proposed and experimentally demonstrated. The concept is illustrated in Fig. 3(c). The device consists of a bow-tie like nanoantenna that interjects the dielectric waveguide, leading to the formation of a 50 nm nanogap at the waveguide center into which a PCM layer (here again GST) is deposited. The device has a somewhat larger IL (roughly 9 dB) with respect to the all-optical solution, but a multi-level memory capability of 20 levels was successfully demonstrated, along with the aforementioned, and very attractive, capability for dual-mode (electrical/optical) operation (which makes interfacing to electronic devices considerably more straightforward). The dual-mode plasmonically enhanced device achieved order-of-magnitude type improvements in switching energies and speeds, as compared to the conventional device architecture.

#### IV. PERSPECTIVES AND POSSIBLE IMPLEMENTATIONS

In Sec. III, we underlined how the use of finely tuned plasmonic nanoantennas, when combined with PCMs, gives access to a previously underexplored manipulation tool that yields useful optical contrast and low IL Si-photonic waveguide devices. A constraint on the operation speed of such devices (i.e., switching from low transmissivity to high transmissivity) remains, as it is here imposed by the phase-change dynamics (primarily the speed limitations imposed by the crystallization process), but single-nanosecond switching should almost certainly be achievable. Moreover, PCM switching is of course non-volatile, making integrated phase-change photonic devices ideal for the realization of photonic memory and (in-memory and neuromorphic) computing devices.

Previous numerical<sup>54,56</sup> and experimental<sup>56</sup> results highlight that the addition of a PCM-embedded nanogap within the plasmonic resonant structure allows one to magnify the light-matter interaction, and so modulate the device optical response in amplitude and, potentially, optical phase and scattering directivity too. The use of a nanoscale volume of PCM, which reduces the volume of material that needs to be heated during switching and so in turn reduces switching energies, would not be effective without a supporting plasmonic nanoantenna (or other resonant cavity). Indeed, the PCM volume reduction, the high volume/surface ratio, and the GST thermal boundary resistance,<sup>57</sup> contribute to a very effective switching process, which for the all-optical plasmonically-enhanced memory device of Gemo *et al.*<sup>54</sup> sees an energy requirement of only  $4.36 \text{ aJ/nm}^3$ , quite close to the theoretical minimum of  $1.9 \text{ aJ/nm}^3$



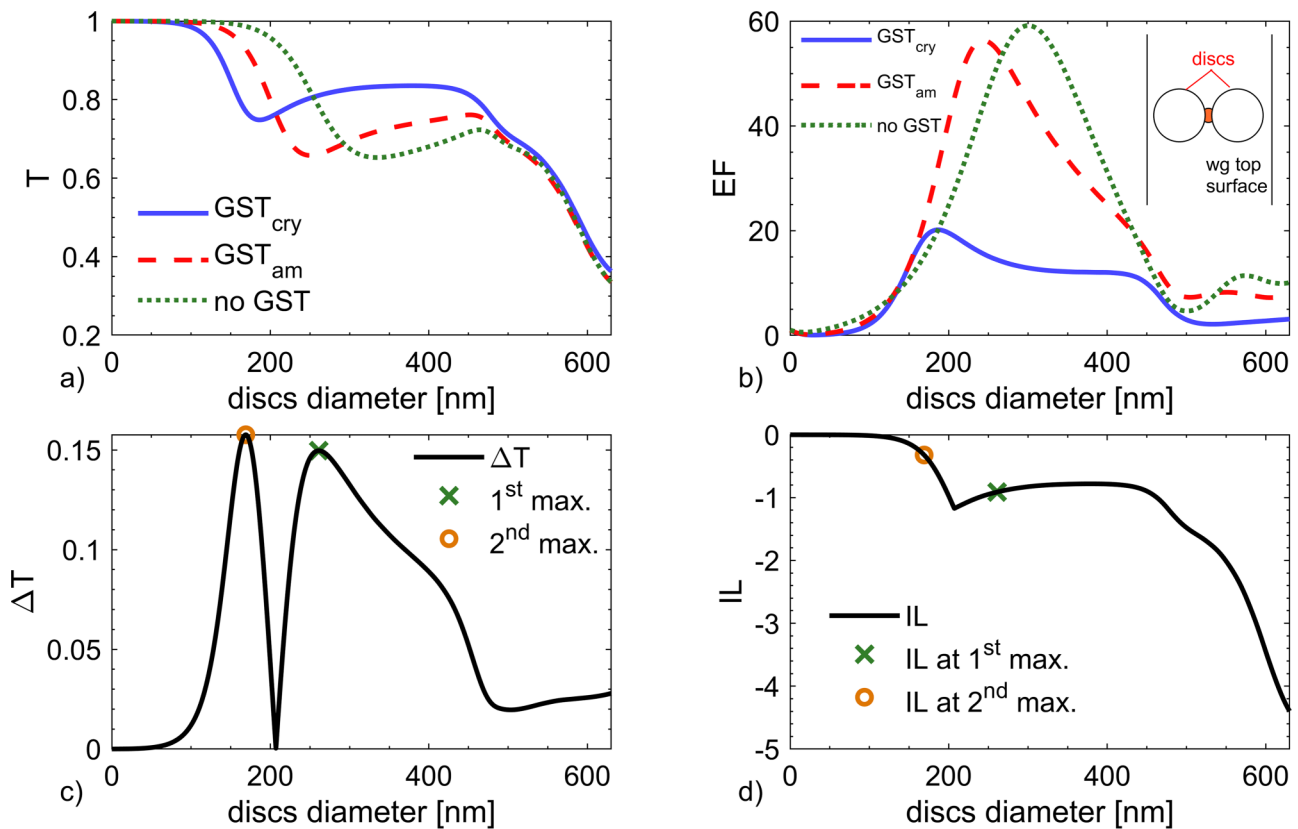
**FIG. 3.** Plasmonically enhanced PCM integrated photonic devices. (a) Nanoring implemented electro-optic modulator. Reproduced with permission from Singh *et al.*, Phys. Lett. A **383**, 3196 (2019). Copyright 2019 Elsevier. (i) Device schematics. (ii) Electric field (normalized) in amorphous (top) and crystal (bottom) phases. (iii) Wavelength- and phase-dependent transmittance. (b) All-photonic phase-change memory. Reproduced with permission from Gemo *et al.* Opt. Express **27**, 24724 (2019). Copyright 2019 Author(s), licensed under Creative Commons Attribution (CC BY) License. (i) Device schematic. (ii) Top view of the device (left), and log of the normalized electric field across the device cut plane for amorphous and crystal GST (right). (iii) and (iv) Overview of overwrite and programming operations. Insets report pulse temporal profiles [the optical contrast is shown as an inset in (iv)]. (c) Mixed-mode photonic memory. Reproduced with permission from Farmakidis *et al.*, Sci. Adv. **5**, eaaw2687 (2019). Copyright 2019 Author(s), licensed under Creative Commons Attribution (CC BY) License. (i) False colored SEM image of the device. (ii) Calculated optical intensity across the device for the amorphous (left) and crystal (right) GST phases. (iii) Wavelength dependency of the optical response. (iv) Switching-energy requirement.

(calculated using values of specific heat, melting temperature, and enthalpy of fusion, as reported in Ref. 54).

One obvious drawback of following a plasmonically enhanced device route for the provision of integrated photonic memory and

computing devices is increased fabrication complexity, which in turn could lead to unacceptable variations in device-to-device properties due to fabrication tolerances. This issue is examined in more detail in Fig. 4 [for the device of the type shown in Fig. 3(b)].

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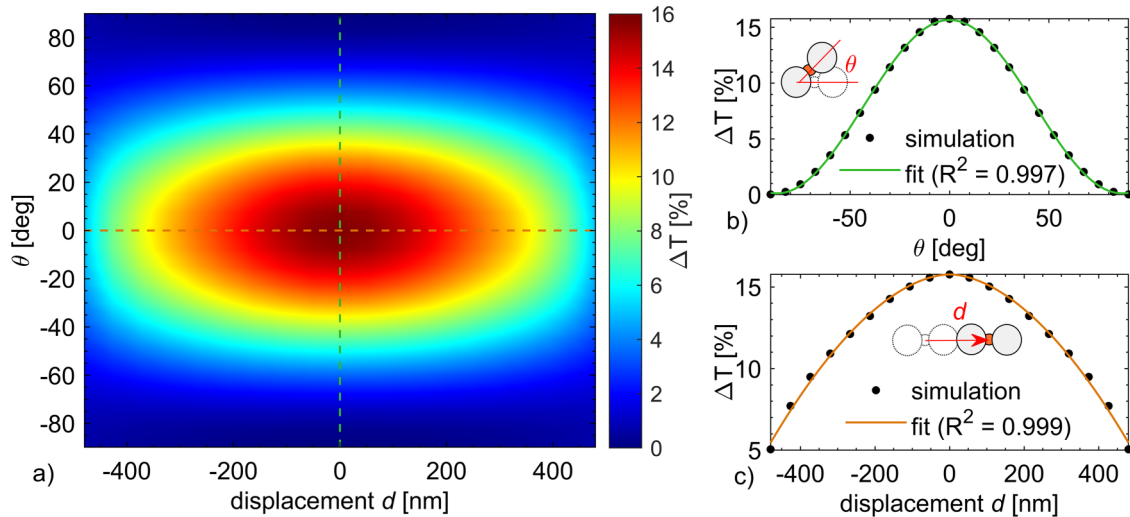
**FIG. 4.** Numerical analysis of the optical characteristics of the dimer-disk plasmonically enhanced photonic PCM device, illustrated in Gemo *et al.*<sup>54</sup> (a) Transmission data, as a function of disk diameter, for the nanoantenna-only configuration (green, dotted line) and GST-implemented configuration (blue and red lines). (b) Field enhancement factor data for the three configurations, calculated at the nanogap location. (c) Optical contrast  $|\Delta T|$  derived from the crystal and amorphous transmission data. (d) Correlated minimum IL data.

Figure 4(a) reports the transmission data, which determine the derived values of optical contrast [see Fig. 4(c)] and IL [Fig. 4(d)]. Specifically, the introduction of the PCM in the nanogap shifts the diameter-dependent resonance feature to shorter values (smaller nanoantenna diameters), correlated with a corresponding shift [see Fig. 4(b)] of the electric field enhancement factor (EF; defined as the squared average e.m. field value in the nanogap divided by the squared peak input e.m. field value). Regardless of the phase of the PCM inclusion, it can be seen that the EF is significant, and this correlates directly with the effective optical intensity perceived by the PCM inclusion, leading to the desired increase in energy efficiency. Two optimal disk diameters for the dimer nanoantenna can be seen [in Fig. 4(c)] at around 170 and 250 nm, the former having a slightly higher transmission modulation ( $\Delta T$ ) and a smaller insertion loss [see Fig. 4(d)]. However, the resonance for a 170 nm disk diameter is quite sharp, while that for 250 nm diameter is significantly broader. Thus, one may opt for the solution allowing for slightly poorer optical performance, yet increasing the fabrication tolerance. Regardless, with the steady advancement of the nanoscale fabrication techniques and establishment of

improved technology nodes, it is possible to foresee how such fabrication barriers will eventually be eroded, possibly allowing one to aim designs toward the more fabrication-critical dimensioning.

We also remark that not all fabrication parameters require fine-tuning, and therefore, the otherwise lengthy fabrication optimization can be limited to the most impactful parameters. As an example, the all-optical plasmonically enhanced device shows a remarkable tolerance in terms of the nanoantenna displacement from, and rotation about, the optimal location at the center of the top surface of the waveguide (see Fig. 5, where both dependencies can be fitted with broad  $\sin^2$  functions).

From the results presented in this section, and in the related literature,<sup>54–56</sup> it is clear that plasmonic enhancement has the potential to drastically improve the energy and speed performance of the conventional integrated phase-change photonic memory devices. Yet, this achievement comes not only at the price of additional fabrication complexity (already discussed), but also at the reduced optical (transmission) contrast (around 20% in the best-optimized configuration). The conventional architecture can achieve much higher contrasts, simply by extending the size (along



**FIG. 5.** Fabrication tolerance analysis of the dimer-disk nanoantenna illustrated in Gemo *et al.*<sup>54</sup> with respect to the displacement  $s$  from the waveguide center and from the tilting angle  $\theta$  from the intended orthogonal orientation. (a) Simulated  $\Delta T$  as a function of displacement and tilt angle. (b) Fit of the data as a function of the tilt angle, calculated at displacement  $s=0$ , as  $\Delta T(\theta) = \Delta T(0) \cdot \sin^2\theta$ . (c) Fit of the data as a function of the displacement, calculated at  $\theta = 0$ , as  $\Delta T(s, \theta) = \Delta T(\theta) \cdot 10^{-6} \cdot (1 + \sin^2\theta) \cdot s^{1.8}$ .

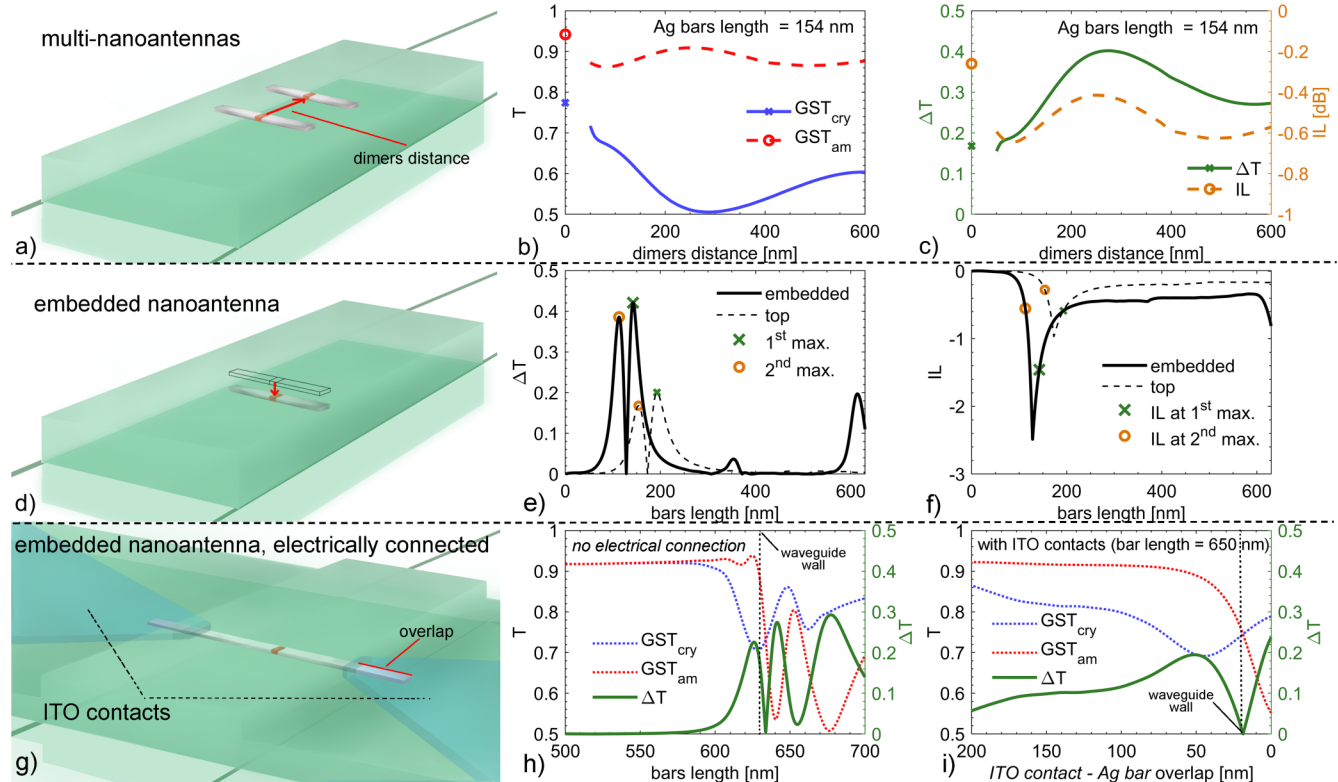
the waveguide) of the PCM unit cell, an unavailable degree of freedom for the plasmonically-enhanced cell. However, there are possible complementary strategies allowing one to not only increase the optical contrast up to (or even surpassing) that of the conventional architecture, but also to further improve device energy performance and switching speed.

One strategy consists of the adoption of multiple plasmonic structures along the waveguide. As a result of the interaction of adjacent structures, a finely tuned center-to-center distance (between successive nanoantennas) allows the establishment of anti-symmetric resonant modes. Such a resonance configuration further suppresses the waveguide mode propagation and increases the e.m. field magnitude in both nanostructures, as previously demonstrated in Jin *et al.*<sup>58</sup> Therefore, two optimally located nanostructures can induce an optical contrast proportionally higher than two non-interacting nanostructures. A preliminary calculation is carried out here for the case of nanoantennas comprising two simple rectangular bars (dimer-bars) separated by a small gap (here of 40 nm), as shown in Fig. 6(a). The transmission data, Fig. 6(b), evidence a non-linear dependency of the waveguide transmission as a function of the center-to-center distance, with peculiar opposite behavior for the amorphous and crystalline cases (i.e., where the crystal case shows a lowering of transmission, the amorphous case shows an increasing transmission). Both the optical contrast,  $\Delta T$ , and insertion loss, IL, benefit significantly from this peculiar interaction [Fig. 6(c)], demonstrating the fact that multiple nanoantenna structures can effectively exploit characteristics offered by localized surface plasmon resonances.

Another strategy that can lead to a remarkable increase in the optical contrast is that of embedding the plasmonic nanoantenna within the body of waveguide, as shown schematically in Fig. 6(d).

The effect of embedding plasmonic antennas within the body of a waveguide was previously explored in simulation by Castro-Lopez *et al.*,<sup>41</sup> though for a case not including phase-change materials. For the case of dimer-bar type antennas, with PCM in the gap region and with the antenna buried at the bottom of the rib section of the waveguide, our calculations [see Figs. 6(e) and 6(f)] show that the maximum optical contrast doubles with respect to that obtained with the device fabricated on the top surface, with only a minor increase in the IL. This is due to the higher interaction of the plasmonic resonant mode with the natural location of the e.m. field peak within the waveguide mode. The enhancement factor here calculated increases to a value of above 100, in place of the value of 20 for the configuration with the nanoantenna on the waveguide's top surface, pointing also toward even further improvements in switching energy and speed for the embedded configuration. The embedded configuration also offers more robust protection against environmental degradation of the plasmonic and phase-change materials used in the device.

The dimer-bar type antenna configuration shown in Fig. 6 also potentially lends itself more easily to the provision of a dual-mode electrical/optical operation, as achieved in the work reported by Farmakidis *et al.*<sup>56</sup> and shown previously in Fig. 3(c), but here using a more conventional dielectric waveguide. For example, by extending the bar antennas toward the edge of the waveguide, it should be possible to make electrical connections to the bars, so that they play a dual role of both a plasmonic nanoantenna and an electrode that would allow the PCM region to be switched (or read out) electrically. One possible design for this is shown in Fig. 6(g), along with its optical performance in Figs. 6(i) and 6(h), from which it can be seen that good optical performance can be achieved (with both optical contrast and IL greatly improved over the hybrid



**FIG. 6.** Plasmonically enhanced integrated photonic phase-change memory concepts increasing the theoretical optical contrast; the examples use a dimer-bars nanoantenna configuration, with gap width, bar width, and thickness of 40, 40, and 30 nm, respectively. (a) Pictorial representation of the double-dimer configuration. (b) and (c) Data obtained for the double-dimer solution using a fixed bars length of 154 nm, as a function of the center-to-center distance [as indicated in (a)]. The degenerate solutions (at distance  $d=0$ ) represent the solution for a single nanostructure, reported as unique markers. (b) Transmission data for the crystalline (blue) and amorphous (red, dashed)  $\text{Ge}_2\text{Sb}_2\text{Te}_5$  PCM inclusion. (c) Resulting optical contrast (left axis, green line; absolute units) and insertion loss (right axis, orange dashes line; dB units) theoretically obtainable after phase switching. (d) Pictorial representation of the device embedded configuration. (e) and (f) Data obtained for the in-waveguide embedded solution. (e) Optical contrast (absolute units) for the embedded configuration (continuous line), compared to the surface-bound device (dashed line). (f) Correlated IL values for the embedded and top configurations. (g) Pictorial representation of the mixed-mode operativity extension for the device in the embedded configuration, comprising two ITO contacts overlapping the Ag bars and interjecting the rib waveguide core. (h) Transmission (left axis) and  $\Delta T$  (right axis) data calculated in the absence of the ITO contacts, for reference. (i) Transmission (left axis) and  $\Delta T$  (right axis) calculated for a fixed bar length of 650 nm, as a function of the ITO contact/Ag bar overlap [as indicated in (g)].

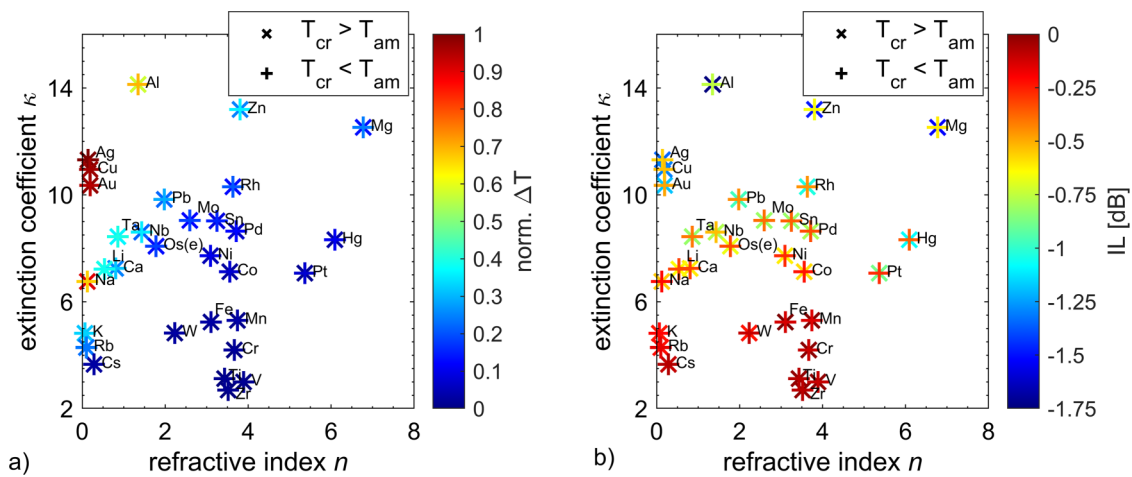
waveguide configuration),<sup>56</sup> while at the same time providing electrical access for potential dual-mode operation.

Finally, we turn our attention to technological issues linked with the fact that the highest performing, and most often used, plasmonic materials are gold and silver, both highly diffusive metals. Indeed, gold is known to diffuse readily into both silicon and chalcogenides, alloying with both to form, for example, gold-silicides and gold-tellurides.<sup>59</sup> This in turn leads to deleterious effects in plasmonic PCM structures, suppressing optical resonances and leading to changes in PCM switching properties (crystallization temperatures). Diffusion, being a temperature-activated process, is worsened by the high-temperature dynamics involved during the PCM phase-switching process in which, for amorphization, the PCM must be heated to above its melting temperature.

To counteract the large diffusivity of high-performing plasmon-supporting metals, a few strategies can be pursued. One obvious approach is to place anti-diffusion barriers<sup>51,59,60</sup> between

the plasmonic material and the PCM and/or any Si or diffusion-susceptible dielectric layers. Of course, it is important that any such a diffusion barrier should not significantly alter, at least in a deleterious way, the optical or thermal properties of the device itself. Silicon nitride ( $\text{Si}_3\text{N}_4$ ) has been shown to be a good barrier choice in this respect, since quite thin layers (<10 nm) can provide sufficient diffusion protection.<sup>59</sup>

A second obvious approach to the problem of the diffusion of plasmonic metals is to search for alternative, non-diffusive options. Any such options should ideally be CMOS-compatible, to allow for, ultimately, easy integration into PIC fabrication lines. Aluminum is one such material: it is a good plasmonic metal<sup>61</sup> and has already been used successfully for a number of CMOS-compatible plasmonic applications.<sup>62–64</sup> However, the melting temperature of Al, at 660 °C, is very close to that of most commonly used PCMs (e.g.,  $\text{Ge}_2\text{Sb}_2\text{Te}_5$  melts at 630 °C,  $\text{Ge}_1\text{Sb}_2\text{Te}_4$  at 614 °C); thus, there is a danger that during the PCM amorphization process any



**FIG. 7.** Optical properties of the dimer (bars) plasmonically enhanced integrated phase-change photonic memory device as a function of the nanoantenna material. Two markers (vertical and diagonal crosses) are used, reporting the two values corresponding to the first and second  $|\Delta T|$  peaks, respectively. (a) Peak  $|\Delta T|$  values (normalized to the maximum value obtained, here for Ag, of 45.9%). (b) IL calculated at the peak  $|\Delta T|$  configuration.

thermally adjacent Al plasmonic layer might also melt, leading to device degradation. To assess the suitability of other metals for integrated phase-change photonic device applications, we have, therefore, explored, in simulation, the optical performance [in terms of achievable (normalized) transmission contrast,  $\Delta T$ , and insertion loss, IL] for a device consisting of a single dimer-bar antenna system [i.e., as shown in 6(d), and with the antenna on the top waveguide surface]. The results are shown in Fig. 7, where, to limit the calculation range, only elemental materials are here considered.

As expected, the noble metals Au and Ag indeed show the best optical performance, with high optical contrast accompanied by a low IL. Aluminum also delivers good optical performance, as does copper; but Al has the previously mentioned problem of low melting point, and Cu is not very CMOS “friendly.” Figure 7 also highlights how nanoantenna materials with optical properties nearest those of a perfect-metal (i.e., low refractive index, high extinction coefficient) exhibit the best performances. While none of the materials examined in Fig. 7 yield optical performances comparable to that of Au, Ag, or Al, two CMOS compatible solutions with reasonable optical performances emerge, namely, tantalum and niobium. Both these elements have attractively high melting temperatures and are relatively inert chemically and physically, and Nb has already been successfully demonstrated for plasmonic (free-space) perfect absorber applications.<sup>65</sup> Further exploration of the potential suitability of Nb and Ta for integrated plasmonic phase-change photonic devices is, thus, recommended.

## V. CONCLUSION

The co-integration of phase-change materials and plasmonics into the silicon photonics platform offers a promising route for the development of fast, low-power, integrated photonic memory and computing devices and systems. Silicon photonics enables low-latency, low-energy, high-bandwidth, and parallelized on-chip

signal transfer; plasmonic resonant structures can efficiently couple with the guided optical mode and squeeze the optical energy down to deep subwavelength features; and phase-change materials, thanks to their inherent non-volatile phase switching capabilities and the large change in optical properties (complex refractive index) that results from such switching, can tune the plasmonic resonant mode, in fact driving the nanoantenna optical response. In this short Perspective paper, we underlined how the optimized combination of these various building blocks can lead to superior performances as compared to conventional, non-plasmonic, device designs. Indeed, improvements in terms of switching energy efficiency and speed in the range of one to two orders of magnitude were obtained. We also showed that the design-space for plasmonically enhanced phase-change memory and computing devices is quite broad, with a wide range of appropriately configured nanoantenna structures (e.g., circular, bar, and bow-tie type antennas; top-surface and embedded antenna types) yielding good optical performance. Moreover, we showed how the electrical and optical domains can be brought together in so-called dual-mode, optical/electrical, device designs, where the PCM cell can be written and read in both domains. This could lead to easier integration of phase-change photonic devices and systems with our currently predominantly electronic computing world.

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## DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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